

TO-247AD

FEATURES:

- Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- Dual rectifier construction, positive centertap
- Low forward voltage, high current capability
- Glass passivated chip junctions
- Low power
- Low forward voltage, high current capability
- High temperature soldering guaranteed :
250°C /10 second, 0.25"(6.35mm) from case

MECHANICAL DATA

Case : JEDEC TO-247 molded plastic

Terminals : Leads solderable per MIL-STD-750

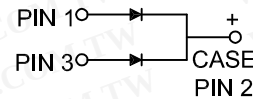
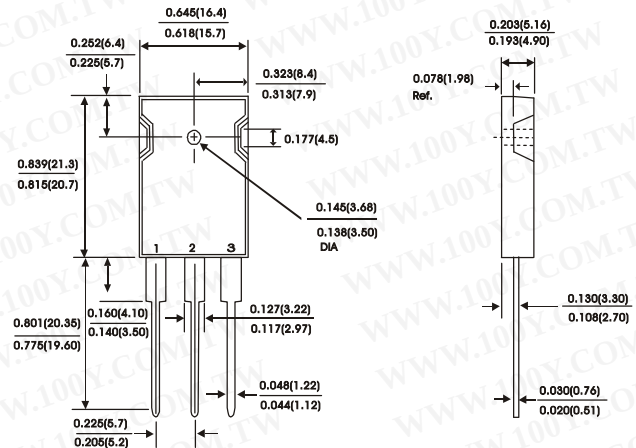
Method 2026

Position : As marked

Mouncting Position : Any

Mouncting Torquce : 5 in - lbs.max

Weight : 0.08 ounce, 2.24grams



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating at 25 °C ambient temperature unless otherwise specified.

Single phase half wave, 60 Hz resistive or inductive load.

For capacitive load, derate current by 20%.

Characteristic	Symbol	SF 30005PT	SF 3001PT	SF 3002PT	SF 3003PT	SF 3004PT	SF 3006PT	Units
Maximum recurrent peak reverse voltage	V _{RRM}	50	100	200	300	400	600	Volts
Maximum RMS voltage	V _{RMS}	35	70	140	210	280	420	Volts
Maximum DC blocking voltage	V _{DC}	50	100	200	300	400	600	Volts
Maximum average forward rectified current at T _c =100°C	I _(AV)	30						Amps
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)(Per leg)	I _{FSM}	300						Amps
Maximum instantaneous forward voltage (Per leg) I _F = 15A	V _F	0.95			1.30		1.70	Volts
Maximum DC reverse current (Per leg) T _c = 25 °C at rated DC blocking voltage T _c = 100°C	I _R	10.0 500.0						μ A
Typical reverse recovery time(NOTE 1)(Per leg)	T _{RR}	35						nS
Typical junction capacition(NOTE 2)(Per leg)	C _J	175.0						P _F
Operating temperature range	T _J	-55to+150						°C
Storage temperature range	T _{Stg}	-55to+150						°C

NOTES:

(1) Reverse Recovery Test CONDITION : $I_F = 0.5A$, $I_R = 1.0A$, $I_{RR} = 0.25A$

(2) Measured at 1MHZ and reverse Voltage of 4.0V

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RATINGS AND CHARACTERISTIC CURVES SF30005PT THRU SF3006PT

FIG.1 - TYPICAL FORWARD CURRENT DERATING CURVE

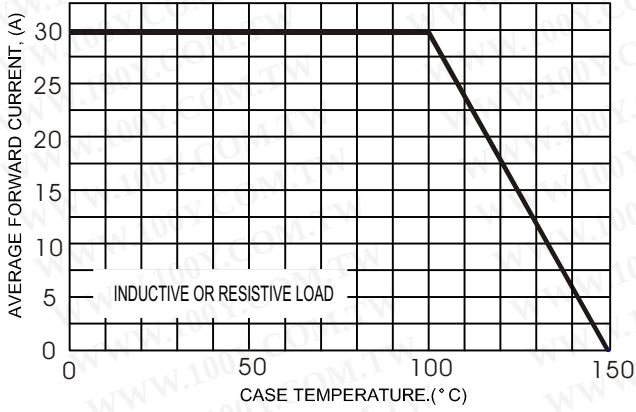


FIG.2 - TYPICAL FORWARD CHARACTERISTICS

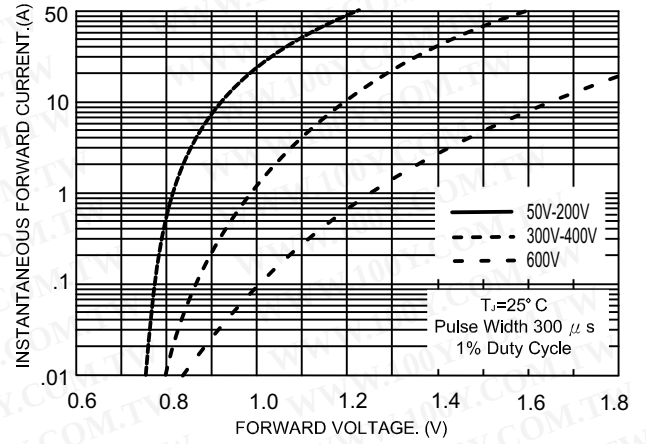


FIG.3 - MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

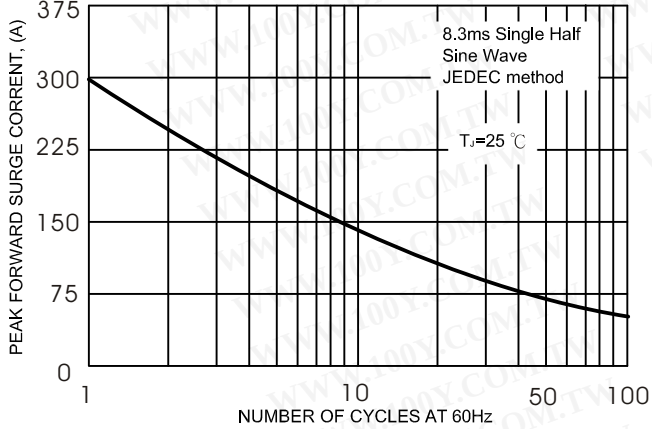


Figure 7 GR1 Test Circuit

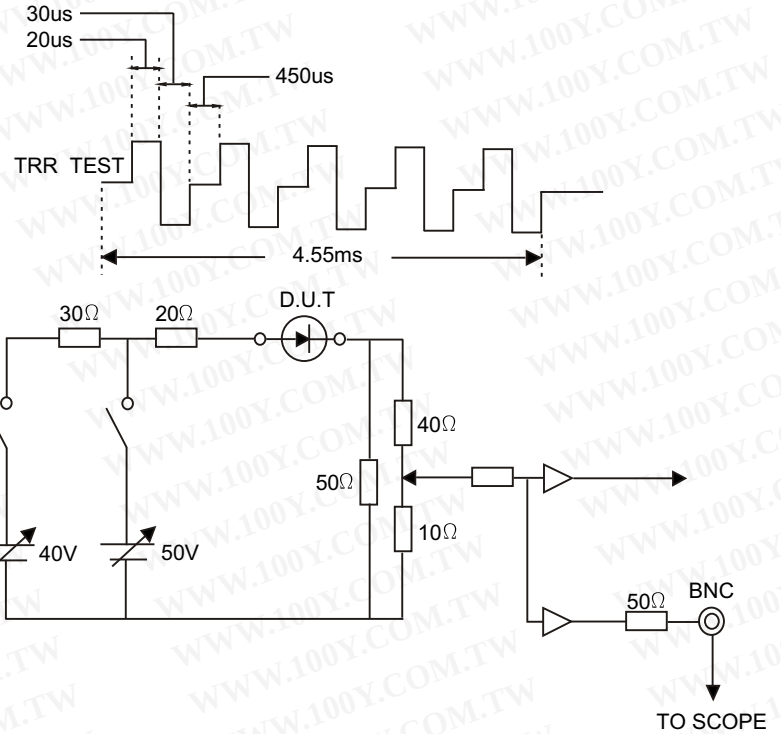


FIG.4- TYPICAL JUNCTION CAPACITANCE

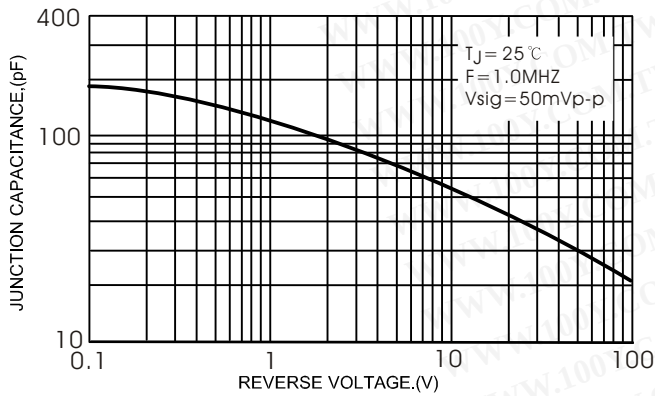
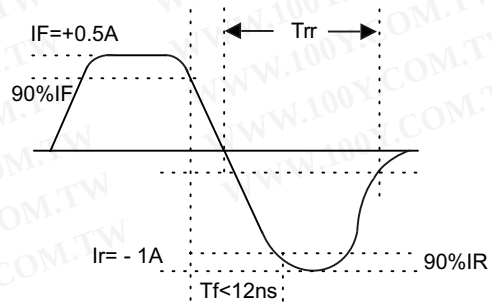
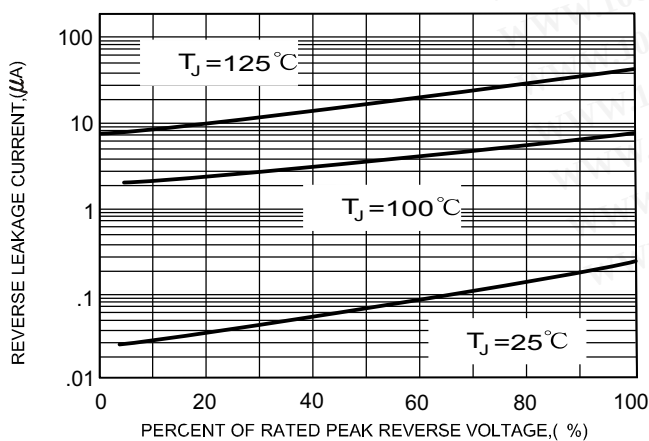


FIG.5- TYPICAL REVERSE CHARACTERISTICS



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