
VOLTAGE DETECTOR

R×5VL SERIES

APPLICATION MANUAL

勝特力材料 886-3-5753170
勝特力电子(上海) 86-21-54151736
勝特力电子(深圳) 86-755-83298787
[Http://www.100y.com.tw](http://www.100y.com.tw)

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ELECTRONIC DEVICES DIVISION

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June 1995

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R \times 5VL SERIES

OUTLINE

The R \times 5VL Series are voltage detector ICs with high detector threshold accuracy and ultra-low supply current by CMOS process. Each of these ICs consists of a voltage reference unit, a comparator, resistors for voltage detection, an output driver and a hysteresis circuit.

The detector threshold is fixed with high accuracy.

Two output Types, Nch open drain type and CMOS type, are available.

Three types of packages, TO-92, SOT-89 (Mini-power Mold), SOT-23-5 (Mini-mold), are available.

FEATURES

- Ultra-low Supply Current.....TYP. 1.0 μ A (VDD=3.0V)
- Broad Operating Voltage Range.....1.5V to 10.0V
- Detector Threshold.....Stepwise setting with a step of 0.1V in the range of 2.0V to 6.0V is possible (refer to Selection Guide).
- High Accuracy Detector Threshold $\pm 2.5\%$
- Low Temperature-Drift Coefficient of Detector ThresholdTYP. ± 100 ppm/ $^{\circ}$ C
- Two Output Types.....Nch Open Drain and CMOS
- Three Types of Packages.....TO-92, SOT-89 (Mini-power Mold), SOT-23-5 (Mini-mold)

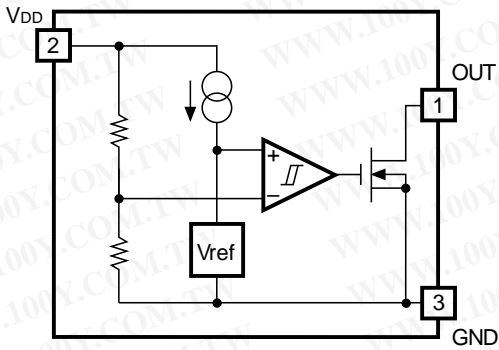
APPLICATIONS

- CPU & Logic Circuit Reset
- Battery Checker
- Window Comparator
- Wave Shaping Circuit
- Battery Back-Up Circuit
- Power Failure Detector

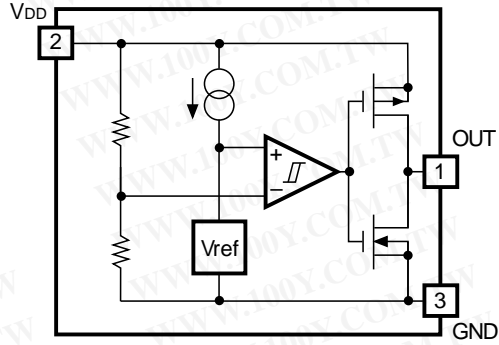
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BLOCK DIAGRAMS

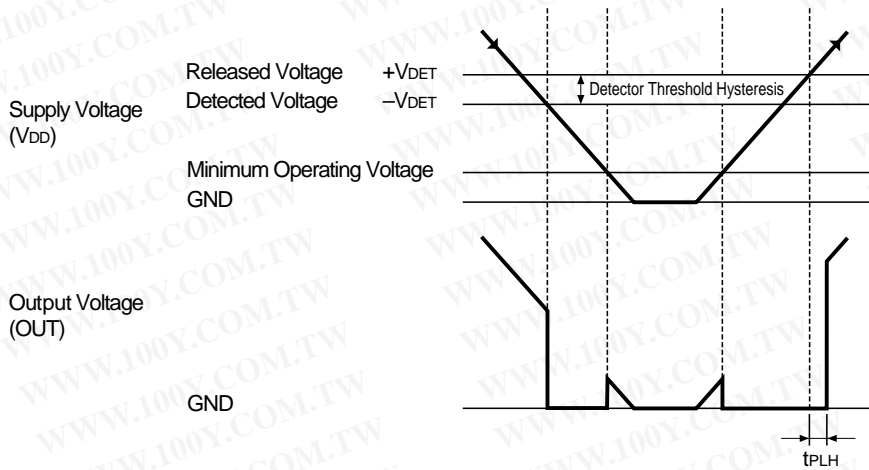
• Nch Open Drain Output (R×5VL×A)



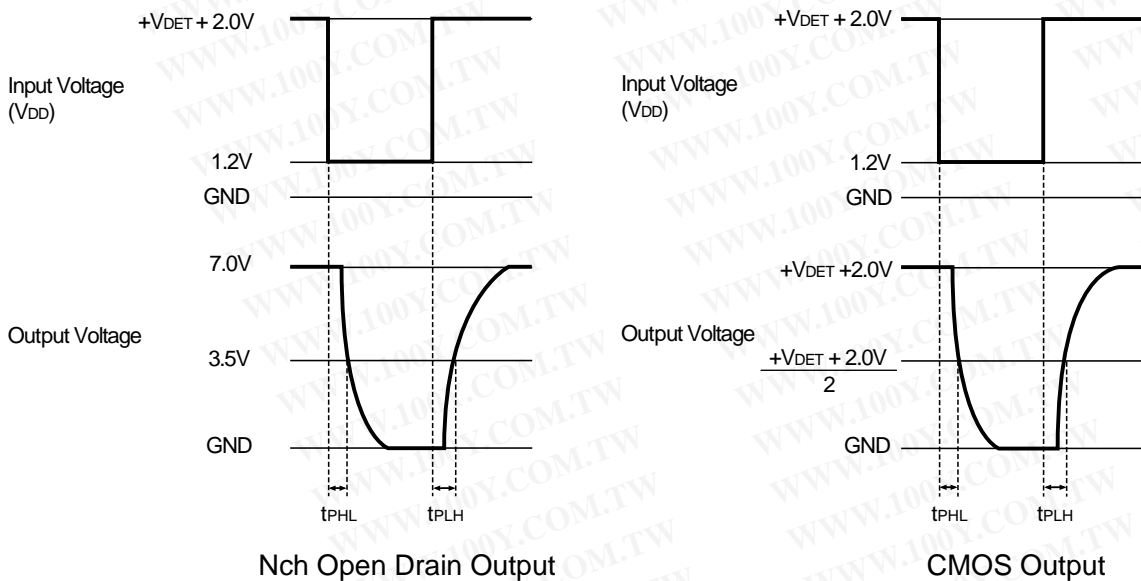
• CMOS Output (R×5VL×C)



TIME CHART



DEFINITION OF OUTPUT DELAY TIME t_{PLH}



Nch Open Drain Output

CMOS Output

Output Delay Time t_{PLH} is defined as follows:

1. In the case of Nch Open Drain Output:

When the time at which a pulse voltage which increases from 1.2V to $+V_{DET}+2.0V$ is applied to VDD is Time A, and the time at which the output reaches 3.5V under the conditions that the output pin (OUT) is pulled up to 7V by a resistor of 100k Ω is Time B, the time period from Time A through Time B.

2. In the case of CMOS Output:

When the time at which a pulse voltage which increases from 1.2V to $+V_{DET}+2.0V$ is applied to VDD is Time A, and the time at which the output voltage reaches the voltage of $(+V_{DET}+2.0V)/2$ is Time B, the time period from Time A through Time B.

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SELECTION GUIDE

The package type, the detector threshold, the output type, the packing type, and the taping type of R5VL series can be designated at the user's request by specifying the part number as follows:

R5VLxxx-xx ← Part Number
 ↑ ↑ ↑ ↑
 a b cd e

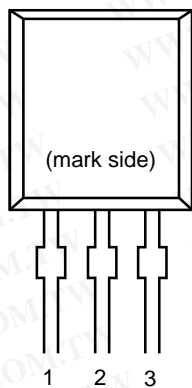
Code	Contents
a	Designation of Package Type: E: TO-92 H: SOT-89 (Mini-power Mold) N: SOT-23-5 (Mini-mold)
b	Setting Detector Threshold (-VDET): Stepwise setting with a step of 0.1V in the range of 2.0V to 6.0V is possible.
c	Designation of Output Type: A: Nch Open Drain C: CMOS
d	Designation of Packing Type: A: Taping C: Antistatic bag for TO-92 and samples
e	Designation of Taping Type: Ex. TO-92: RF, RR, TZ SOT-89: T1, T2 SOT-23-5: TR, TL (refer to Taping Specifications) "TZ", "T1" and "TR" are prescribed as a standard.

For example, the product with Package Type SOT-89, Detector Threshold 3.5V, Output Type Nch Open Drain and Taping Type T1, is designated by Part Number RH5VL35AA-T1.

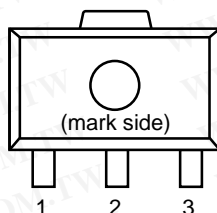
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PIN CONFIGURATION

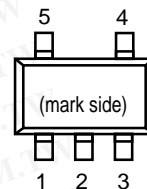
• TO-92



• SOT-89



• SOT-23-5



PIN DESCRIPTION

• TO-92

Pin No	Symbol
1	OUT
2	VDD
3	GND

• SOT-89

Pin No	Symbol
1	OUT
2	VDD
3	GND

• SOT-23-5

Pin No	Symbol
1	OUT
2	VDD
3	GND
4	NC
5	NC

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ABSOLUTE MAXIMUM RATINGS

T_{opt}=25°C

Symbol	Item	Rating	Unit
VDD	Supply Voltage	12	V
V _{OUT}	Output Voltage	CMOS	V _{SS} -0.3 to V _{DD} +0.3
		Nch	V _{SS} -0.3 to 12
I _{OUT}	Output Current	70	mA
PD1	Power Dissipation 1 (NOTE1)	300	mW
PD2	Power Dissipation 2 (NOTE2)	150	mW
T _{opt}	Operating Temperature Range	-30 to +80	°C
T _{stg}	Storage Temperature Range	-55 to +125	°C
T _{solder}	Lead Temperature (Soldering)	260°C,10s	

(NOTE 1) applied to SOT-89 and TO-92

(NOTE 2) applied to SOT-23-5

ABSOLUTE MAXIMUM RATINGS

Absolute Maximum ratings are threshold limit values that must not be exceeded even for an instant under any conditions. Moreover, such values for any two items must not be reached simultaneously. Operation above these absolute maximum ratings may cause degradation or permanent damage to the device. These are stress ratings only and do not necessarily imply functional operation below these limits.

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ELECTRICAL CHARACTERISTICS

• R5VL20C

Topt=25°C

Symbol	Item	Conditions	MIN.	TYP.	MAX.	Unit
-VDET	Detector Threshold		1.950	2.000	2.050	V
VHYS	Detector Threshold Hysteresis		0.060	0.100	0.140	V
ISS	Supply Current	VDD=1.90V		0.90	2.70	μA
		VDD=4.00V		1.10	3.30	
		VDD=10.0V		1.70	5.10	
VDD	Operating Voltage		1.50		10.0	V
IOUT	Output Current	Nch VDS=0.5V VDD=1.5V	0.25	0.50		mA
		Pch VDS=-2.1V VDD=4.5V	1.00	2.00		mA
tPLH	Output Delay Time				100	μs
$\frac{\Delta-VDET}{\Delta Topt}$	Detector Threshold Temperature Coefficient	-30°C ≤ Topt ≤ 80°C		±100		ppm/°C

• R5VL27C

Topt=25°C

Symbol	Item	Conditions	MIN.	TYP.	MAX.	Unit
-VDET	Detector Threshold		2.633	2.700	2.767	V
VHYS	Detector Threshold Hysteresis		0.081	0.135	0.189	V
ISS	Supply Current	VDD=2.60V		0.90	2.70	μA
		VDD=4.70V		1.10	3.30	
		VDD=10.0V		1.70	5.10	
VDD	Operating Voltage		1.50		10.0	V
IOUT	Output Current	Nch VDS=0.5V VDD=1.5V	0.25	0.50		mA
		Nch VDS=0.5V VDD=2.0V	1.50	3.00		mA
		Pch VDS=-2.1V VDD=4.5V	1.00	2.00		mA
tPLH	Output Delay Time				100	μs
$\frac{\Delta-VDET}{\Delta Topt}$	Detector Threshold Temperature Coefficient	-30°C ≤ Topt ≤ 80°C		±100		ppm/°C

R5VL

• R5VL36C

T_{opt}=25°C

Symbol	Item	Conditions	MIN.	TYP.	MAX.	Unit
-V _{DET}	Detector Threshold		3.510	3.600	3.690	V
V _{HYS}	Detector Threshold Hysteresis		0.108	0.180	0.252	V
I _{SS}	Supply Current	V _{DD} =3.47V		1.00	3.00	μA
		V _{DD} =5.60V		1.20	3.60	
		V _{DD} =10.0V		1.70	5.10	
V _{DD}	Operating Voltage		1.50		10.0	V
I _{OUT}	Output Current	Nch V _{DS} =0.5V V _{DD} =1.5V	0.25	0.50		mA
		Nch V _{DS} =0.5V V _{DD} =3.0V	3.00	5.00		mA
		Pch V _{DS} =-2.1V V _{DD} =4.5V	1.00	2.00		mA
t _{PLH}	Output Delay Time				100	μs
$\frac{\Delta -V_{DET}}{\Delta T_{opt}}$	Detector Threshold Temperature Coefficient	-30°C ≤ T _{opt} ≤ 80°C		±100		ppm/°C

• R5VL45C

T_{opt}=25°C

Symbol	Item	Conditions	MIN.	TYP.	MAX.	Unit
-V _{DET}	Detector Threshold		4.388	4.500	4.612	V
V _{HYS}	Detector Threshold Hysteresis		0.135	0.225	0.315	V
I _{SS}	Supply Current	V _{DD} =4.34V		1.10	3.30	μA
		V _{DD} =6.50V		1.30	3.90	
		V _{DD} =10.0V		1.70	5.10	
V _{DD}	Operating Voltage		1.50		10.0	V
I _{OUT}	Output Current	Nch V _{DS} =0.5V V _{DD} =1.5V	0.25	0.50		mA
		Nch V _{DS} =0.5V V _{DD} =4.0V	4.00	6.00		mA
		Pch V _{DS} =-2.1V V _{DD} =8.0V	1.50	3.00		mA
t _{PLH}	Output Delay Time				100	μs
$\frac{\Delta -V_{DET}}{\Delta T_{opt}}$	Detector Threshold Temperature Coefficient	-30°C ≤ T _{opt} ≤ 80°C		±100		ppm/°C

• R5VL54C

T_{opt}=25°C

Symbol	Item	Conditions	MIN.	TYP.	MAX.	Unit
-V _{DET}	Detector Threshold		5.265	5.400	5.535	V
V _{HYS}	Detector Threshold Hysteresis		0.162	0.270	0.378	V
I _{SS}	Supply Current	V _{DD} =5.20V		1.20	3.60	μA
		V _{DD} =7.40V		1.40	4.20	
		V _{DD} =10.0V		1.70	5.10	
V _{DD}	Operating Voltage		1.50		10.0	V
I _{OUT}	Output Current	Nch V _{DS} =0.5V V _{DD} =1.5V	0.25	0.50		mA
		Nch V _{DS} =0.5V V _{DD} =5.0V	5.00	7.00		mA
		Pch V _{DS} =-2.1V V _{DD} =8.0V	1.50	3.00		mA
t _{PLH}	Output Delay Time				100	μs
$\frac{\Delta-V_{DET}}{\Delta T_{opt}}$	Detector Threshold Temperature Coefficient	-30°C ≤ T _{opt} ≤ 80°C		±100		ppm/°C

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ELECTRICAL CHARACTERISTICS BY DETECTOR THRESHOLD

• R5VLxA

Part Number	Detector Threshold			Detector Threshold Hysteresis		Supply Current 1			Supply Current 2			Supply Current 3		
	-V _{DET} (V)			V _{HYS} (V)		I _{SS} (μ A)			I _{SS} (μ A)			I _{SS} (μ A)		
	MIN.	TYP.	MAX.	MIN.	MAX.	Conditions	TYP.	MAX.	Conditions	TYP.	MAX.	Conditions	TYP.	MAX.
R5VL20A	1.950	2.000	2.050											
R5VL21A	2.048	2.100	2.152											
R5VL22A	2.145	2.200	2.255											
R5VL23A	2.243	2.300	2.357											
R5VL24A	2.340	2.400	2.460											
R5VL25A	2.438	2.500	2.562											
R5VL26A	2.535	2.600	2.665											
R5VL27A	2.633	2.700	2.767											
R5VL28A	2.730	2.800	2.870											
R5VL29A	2.828	2.900	2.972											
R5VL30A	2.925	3.000	3.075											
R5VL31A	3.023	3.100	3.177											
R5VL32A	3.120	3.200	3.280											
R5VL33A	3.218	3.300	3.382											
R5VL34A	3.315	3.400	3.485											
R5VL35A	3.413	3.500	3.587											
R5VL36A	3.510	3.600	3.690											
R5VL37A	3.608	3.700	3.792											
R5VL38A	3.705	3.800	3.895											
R5VL39A	3.803	3.900	3.997	(-V _{DET})	(-V _{DET})				V _{DD} =			V _{DD} =		
R5VL40A	3.900	4.000	4.100	$\times 3\%$	$\times 7\%$				(-V _{DET})			10V	1.7	5.1
R5VL41A	3.998	4.100	4.202						+2.0V					
R5VL42A	4.095	4.200	4.305											
R5VL43A	4.193	4.300	4.407											
R5VL44A	4.290	4.400	4.510											
R5VL45A	4.388	4.500	4.612											
R5VL46A	4.485	4.600	4.715											
R5VL47A	4.583	4.700	4.817											
R5VL48A	4.680	4.800	4.920											
R5VL49A	4.778	4.900	5.022											
R5VL50A	4.875	5.000	5.125											
R5VL51A	4.973	5.100	5.227											
R5VL52A	5.070	5.200	5.330											
R5VL53A	5.168	5.300	5.432											
R5VL54A	5.265	5.400	5.535											
R5VL55A	5.363	5.500	5.637											
R5VL56A	5.460	5.600	5.740											
R5VL57A	5.558	5.700	5.842											
R5VL58A	5.655	5.800	5.945											
R5VL59A	5.753	5.900	6.047											

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T_{opt}=25°C

Output Current 1			Output Current 2			Output Delay Time	Operating Voltage		Detector Threshold Tempco.	
I _{out} (mA)			I _{out} (mA)			t _{PLH} (μs)	V _{DD} (V)		Δ-V _{DET} /ΔT _{opt} (ppm/°C)	
Conditions	MIN.	TYP.	Conditions	MIN.	TYP.	MAX.	MIN.	MAX.	Conditions	TYP.
Nch V _{DS} = 0.5V V _{DD} = 1.5V	0.25	0.50	—	—	—	100	1.5	10	-30°C ≤ T _{opt} ≤ 80°C	±100
			Nch	1.5	3.0					
			Nch	3.0	5.0					
			Nch	4.0	6.0					
Nch			Nch	5.0	7.0					

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R5VL

• R5VLxC

Part Number	Detector Threshold			Detector Threshold Hysteresis		Supply Current 1			Supply Current 2			Supply Current 3			
	-VDET(V)			VHYS(V)		Iss(μA)			Iss(μA)			Iss(μA)			
	MIN.	TYP.	MAX.	MIN.	MAX.	Conditions	TYP.	MAX.	Conditions	TYP.	MAX.	Conditions	TYP.	MAX.	
R5VL20C	1.950	2.000	2.050												
R5VL21C	2.048	2.100	2.152												
R5VL22C	2.145	2.200	2.255												
R5VL23C	2.243	2.300	2.357												
R5VL24C	2.340	2.400	2.460												
R5VL25C	2.438	2.500	2.562												
R5VL26C	2.535	2.600	2.665												
R5VL27C	2.633	2.700	2.767												
R5VL28C	2.730	2.800	2.870												
R5VL29C	2.828	2.900	2.972												
R5VL30C	2.925	3.000	3.075												
R5VL31C	3.023	3.100	3.177												
R5VL32C	3.120	3.200	3.280												
R5VL33C	3.218	3.300	3.382												
R5VL34C	3.315	3.400	3.485												
R5VL35C	3.413	3.500	3.587												
R5VL36C	3.510	3.600	3.690												
R5VL37C	3.608	3.700	3.792												
R5VL38C	3.705	3.800	3.895												
R5VL39C	3.803	3.900	3.997	(-VDET)	(-VDET)				VDD=			VDD=			
R5VL40C	3.900	4.000	4.100	×3%	×7%				(-VDET)			+2.0V	10V	1.7	5.1
R5VL41C	3.998	4.100	4.202												
R5VL42C	4.095	4.200	4.305												
R5VL43C	4.193	4.300	4.407												
R5VL44C	4.290	4.400	4.510												
R5VL45C	4.388	4.500	4.612												
R5VL46C	4.485	4.600	4.715												
R5VL47C	4.583	4.700	4.817												
R5VL48C	4.680	4.800	4.920												
R5VL49C	4.778	4.900	5.022												
R5VL50C	4.875	5.000	5.125												
R5VL51C	4.973	5.100	5.227												
R5VL52C	5.070	5.200	5.330												
R5VL53C	5.168	5.300	5.432												
R5VL54C	5.265	5.400	5.535												
R5VL55C	5.363	5.500	5.637												
R5VL56C	5.460	5.600	5.740												
R5VL57C	5.558	5.700	5.842												
R5VL58C	5.655	5.800	5.945												
R5VL59C	5.753	5.900	6.047												

Topt=25°C

Output Current 1			Output Current 2			Output Current 3			Output Delay Time	Operating Voltage		Detector Threshold Tempco.	
Iout(mA)			Iout(mA)			Iout(mA)			tPLH (μs)	VDD(V)		Δ-VoET/ΔTopt (ppm/°C)	
Conditions	MIN.	TYP.	Conditions	MIN.	TYP.	Conditions	MIN.	TYP.	MAX.	MIN.	MAX.	Conditions	TYP.
Nch	0.25	0.50	—	—	—	Pch	1.0	2.0	100	1.5	10	-30°C ≤ Topt ≤ 80°C	±100
			Nch	VDS= 1.5	3.0								
			VDS= 0.5V			VDD= 2.0V							
			VDD= 0.5V			VDD= 4.5V							
Nch			Nch	3.0	5.0	Pch	1.5	3.0					
			VDS= 0.5V										
Nch			VDS= 3.0	5.0		Pch	1.5	3.0					
			VDD= 3.0V										
Nch			Nch	4.0	6.0	Pch	1.5	3.0					
			VDS= 0.5V										
Nch			VDS= 0.5V			Pch	1.5	3.0					
			VDD= 4.0V										
Nch			Nch	5.0	7.0	Pch	1.5	3.0					
			VDS= 0.5V										
Nch			VDS= 0.5V			Pch	1.5	3.0					
			VDD= 5.0V										

OPERATION

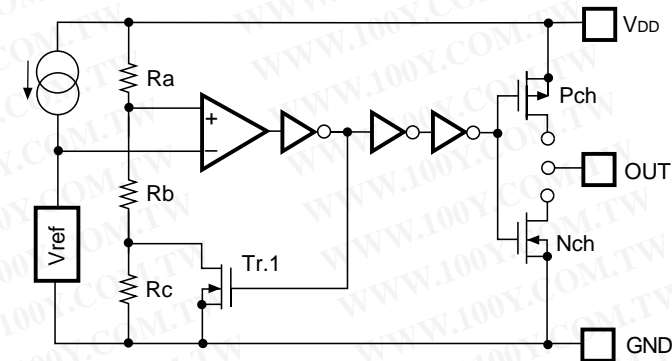


FIG. 1 Block Diagram

- In R5VLxA, Nch Tr. drain is connected to OUT pin.
- In R5VLxC, Nch Tr. drain and Pch Tr. drain are connected to OUT pin.

Operation Diagram

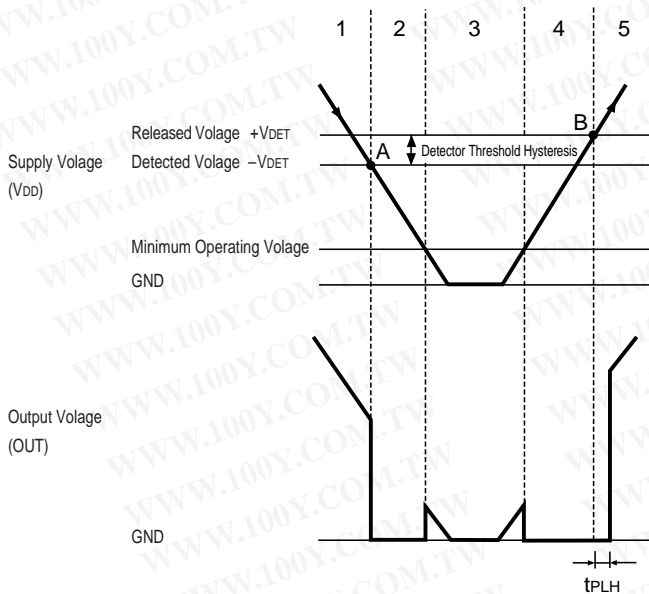


FIG. 2 Operation Diagram

Step	Step 1	Step 2	Step 3	Step 4	Step 5	
Comparator(+)Pin Input Voltage	I	II	II	II	I	
Comparator Output	H	L	Indefinite	L	H	
Tr. 1	OFF	ON	Indefinite	ON	OFF	
Output Tr.	Pch	ON	OFF	Indefinite	OFF	ON
	Nch	OFF	ON	Indefinite	ON	OFF

$$I. \frac{R_b + R_c}{R_a + R_b + R_c} \cdot V_{DD}$$

$$II. \frac{R_b}{R_a + R_b} \cdot V_{DD}$$

- Step 1. Output Voltage is equal to Power Source Voltage (VDD).
- Step 2. When Input Voltage to Comparator reaches the state of $V_{ref} \geq V_{DD} \cdot (R_b + R_c) / (R_a + R_b + R_c)$ at Point A (Detected Voltage $-V_{DET}$), the output of Comparator is reserved, so that Output Voltage becomes GND.
- Step 3. In the case of CMOS Output, Output Voltage becomes unstable when Supply Voltage (VDD) is smaller than Minimum Operating Voltage. In the case of Nch Open Drain Output, a pulled-up voltage is output.
- Step 4. Output Voltage becomes equal to GND.
- Step 5. When Input Voltage to Comparator reaches the state of $V_{ref} \leq V_{DD} \cdot (R_b) / (R_a + R_b)$ at Point B (Released Voltage $+V_{DET}$), the output of Comparator is reserved, so that Output Voltage becomes equal to Supply Voltage (VDD)

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TEST CIRCUITS

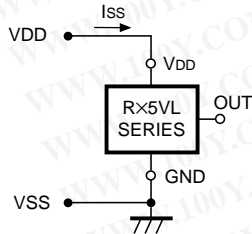


FIG. 3 Supply Current Test Circuit

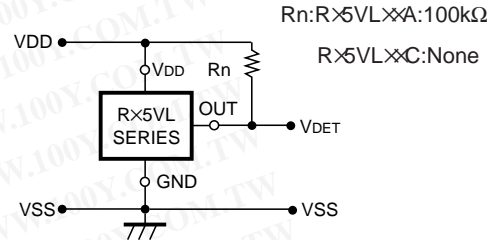


FIG. 4 Detector Threshold Test Circuit

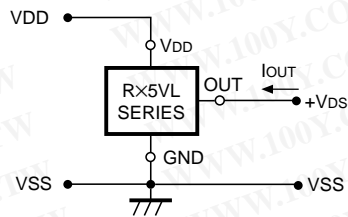


FIG. 5 Nch Driver Output Current Test Circuit

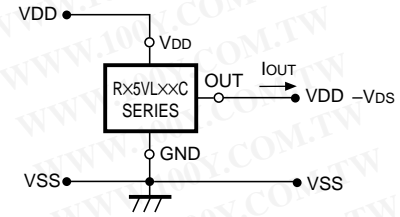


FIG. 6 Pch Driver Output Current Test Circuit

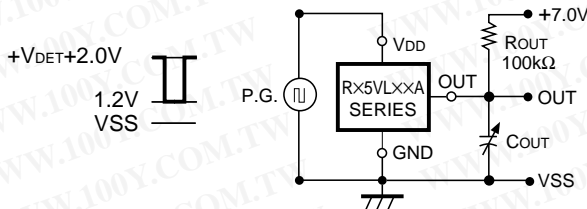
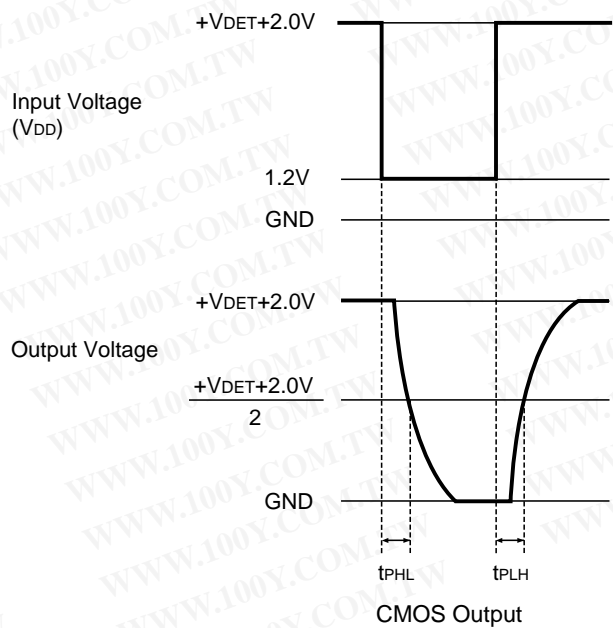
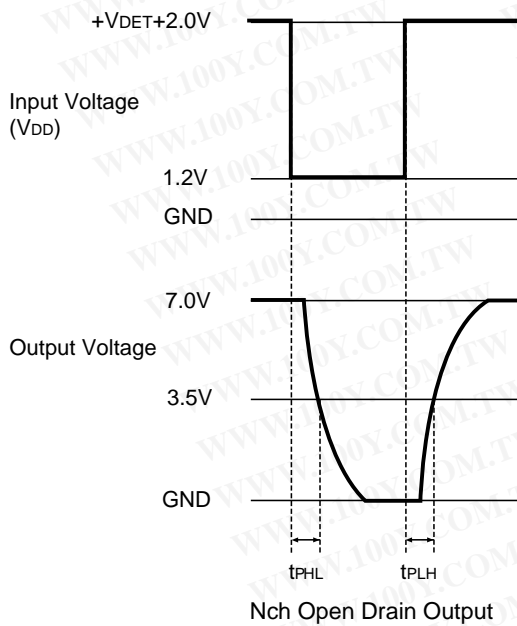


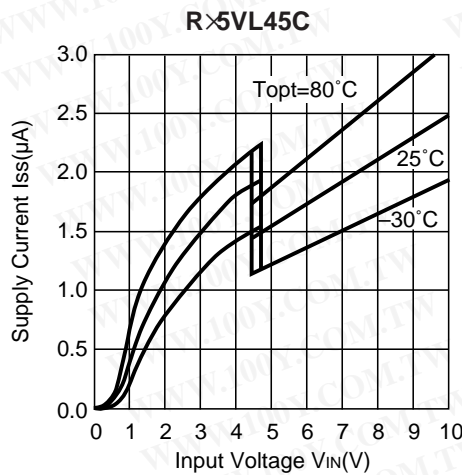
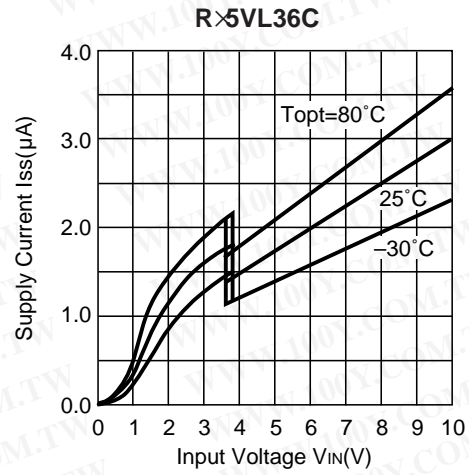
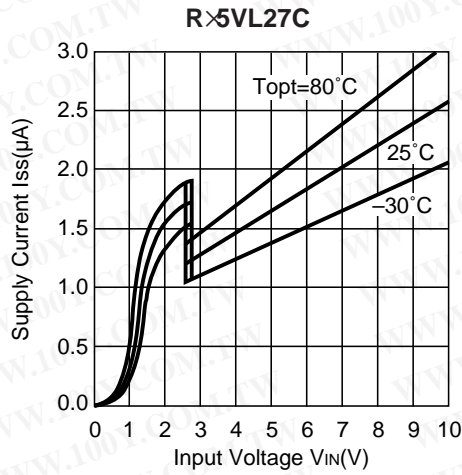
FIG. 7 Output Delay Time Test Circuit

In Output Delay Time Test Circuit in FIG.7, it's Output Voltage Fall Times (t_{PHL}) and Rise Times (t_{PLH}) are defined as shown below.



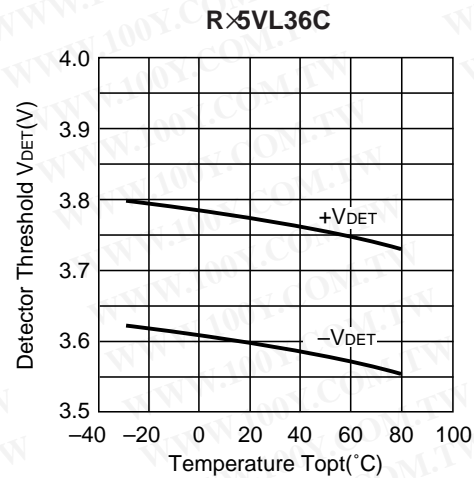
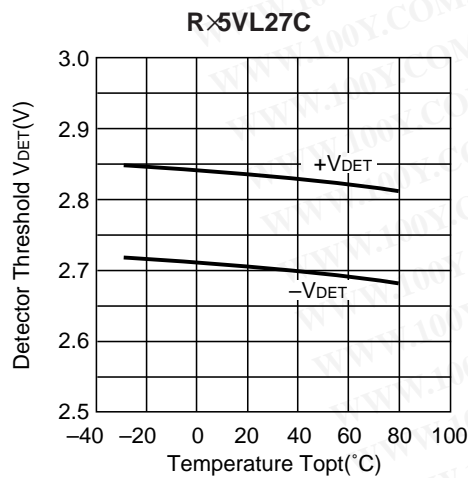
TYPICAL CHARACTERISTICS

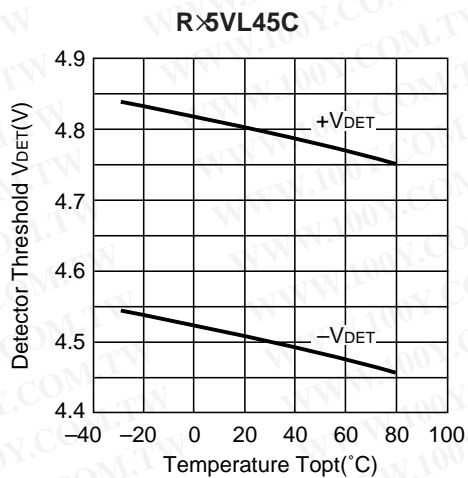
1) Supply Current vs. Input Voltage



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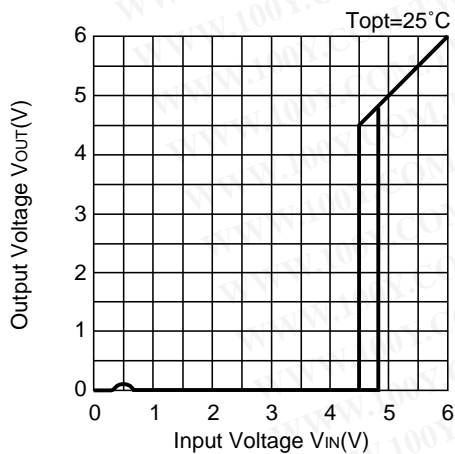
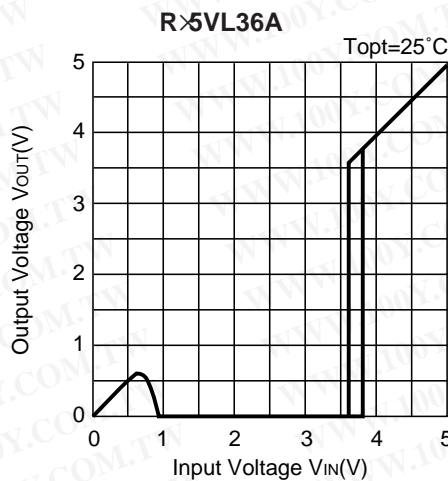
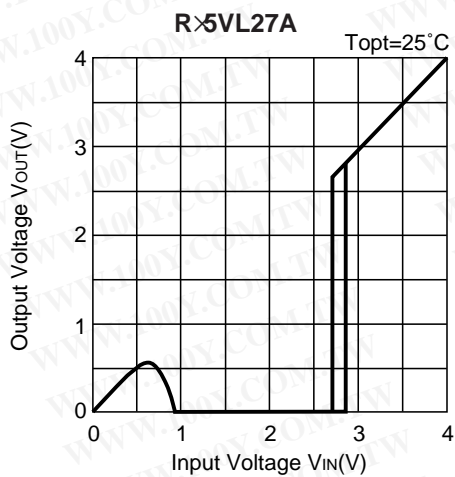
2) Detector Threshold vs. Temperature



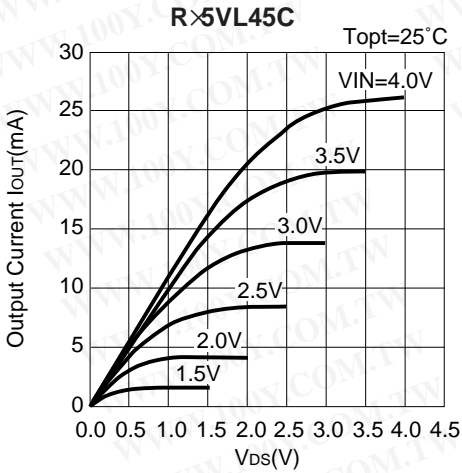
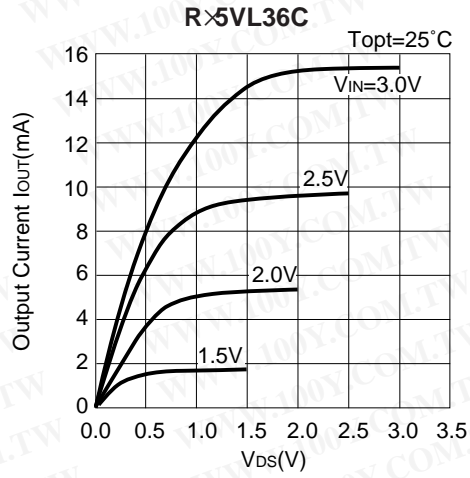
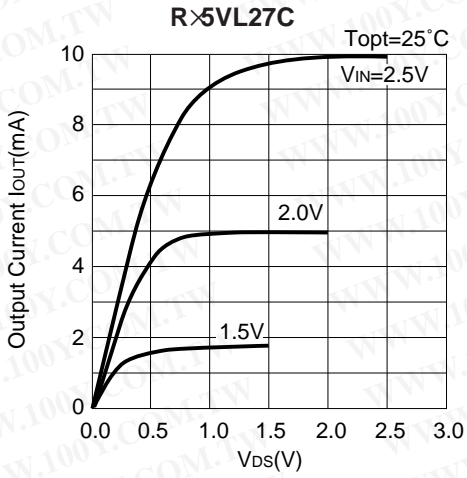


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3) Output Voltage vs. Input Voltage

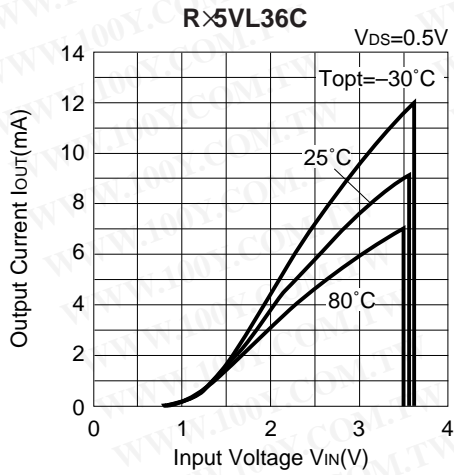
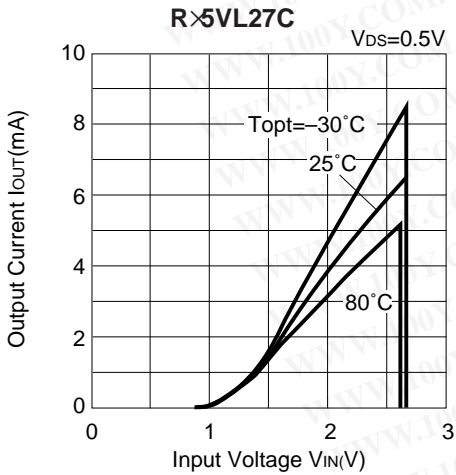


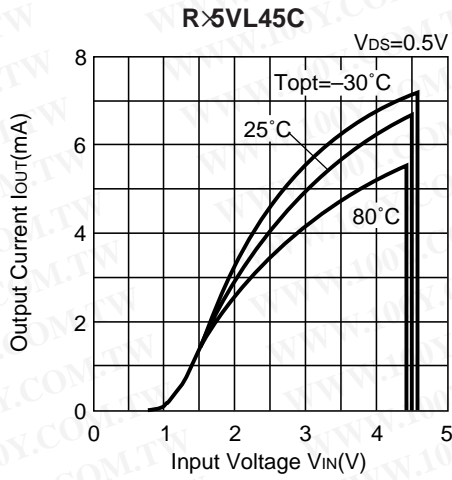
4) Nch Driver Output Current vs. Vds



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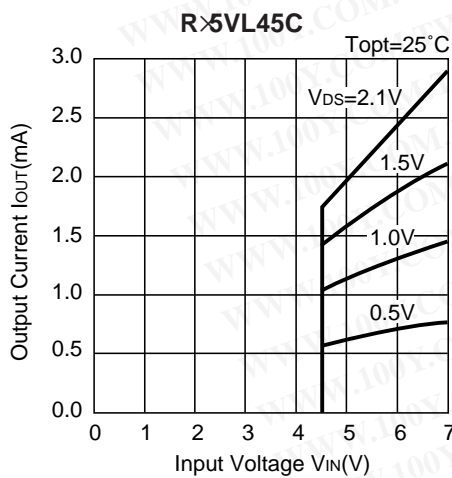
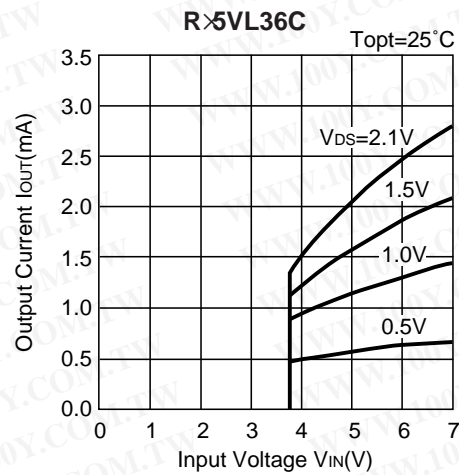
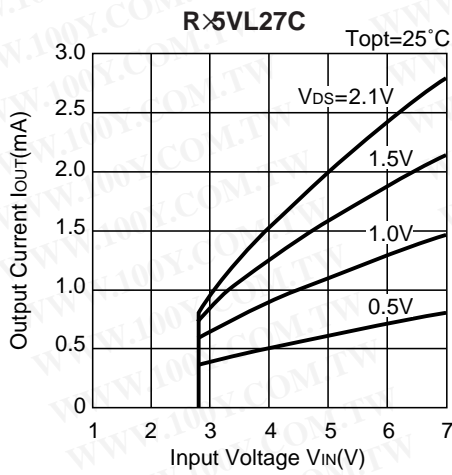
5) Nch Driver Output Current vs. Input Voltage



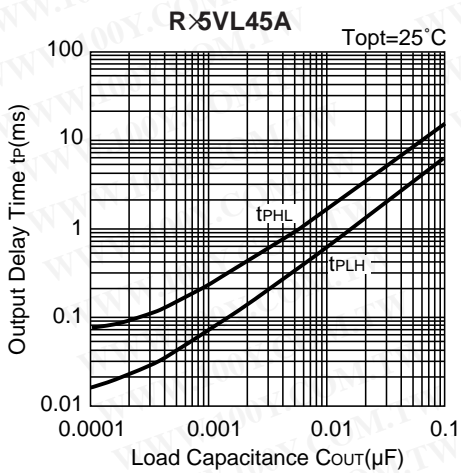
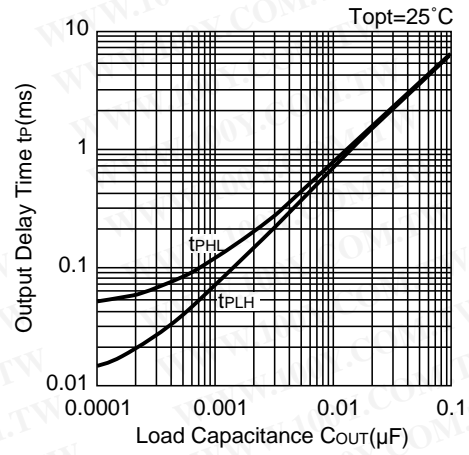
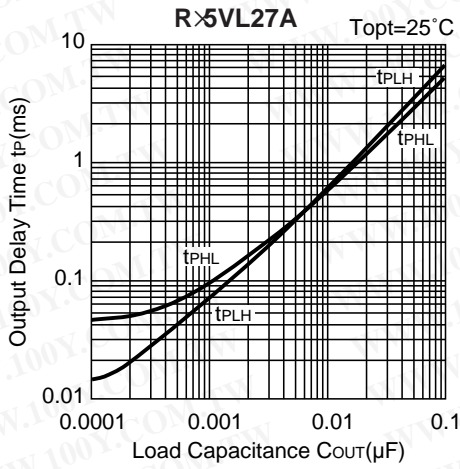


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6) Pch Driver Output Current vs. Input Voltage



7) Output Delay Time vs. Load Capacitance

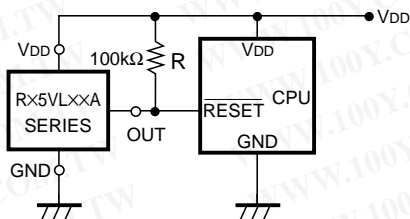


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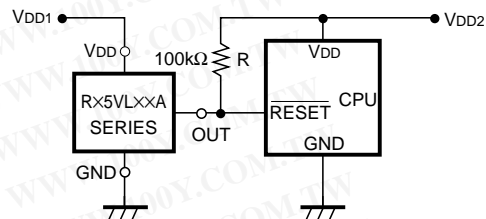
TYPICAL APPLICATION

• R5VLxA CPU Reset Circuit (Nch Open Drain Output)

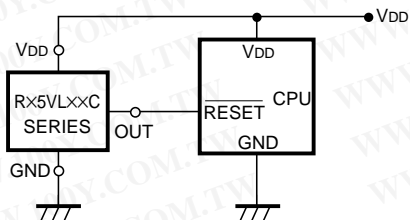
(1) Input Voltage to R5VLxA is the same as the input voltage to CPU.



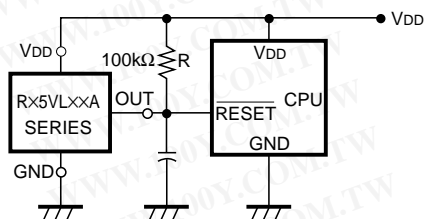
(2) Input Voltage to R5VLxA is different from the input voltage to CPU.



• R5VLxC CPU Reset Circuit (CMOS Output)

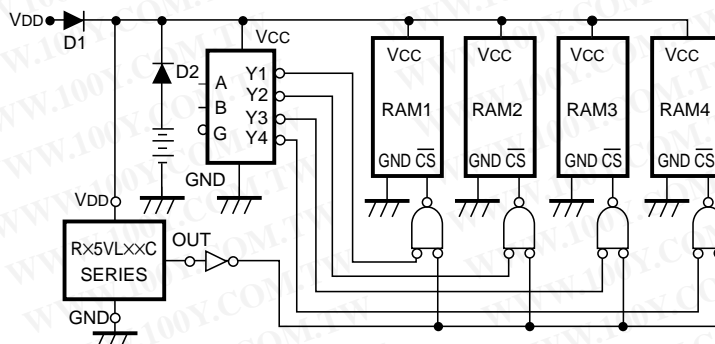


• R5VLxA Output delay Time Circuit

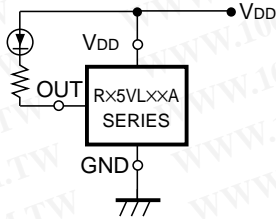


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• Memory Back-up Circuit

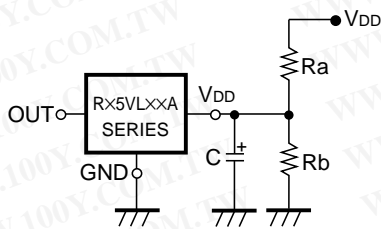


• Voltage Level Indicator Circuit (lighted when the power runs out)
(Nch Open Drain Output)



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• Detector Threshold Changing Circuit
(Nch Open Drain Output)

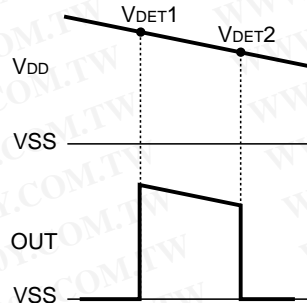
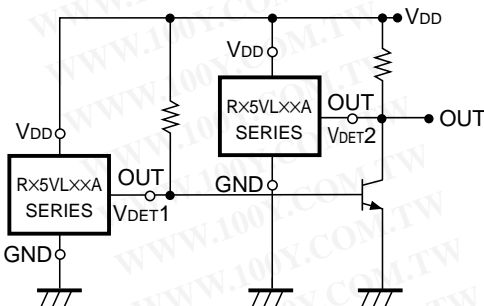


$$\text{Changed Detector Threshold} = \frac{Ra + Rb}{Rb} \cdot (-V_{DET})$$

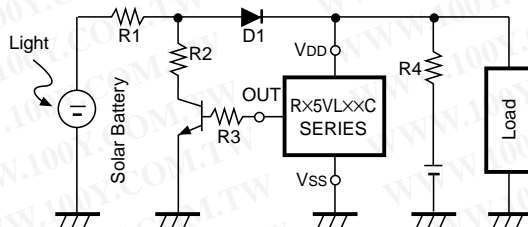
$$\text{Hysteresis Voltage} = \frac{Ra + Rb}{Rb} \cdot V_{HYS}$$

(Note) Please note that when the value of Ra becomes excessively large, the detector threshold detected may differ from the value calculated by use of the above formula.

• Window Comparator Circuit
(Nch Open Drain Output)



• Excessive Charge Preventing Circuit



APPLICATION HINTS

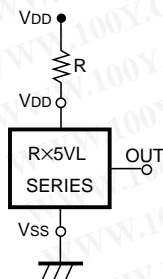


FIG. 8

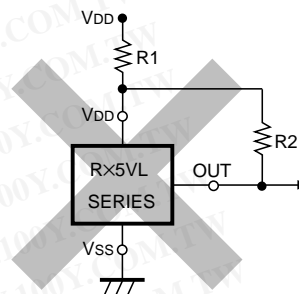


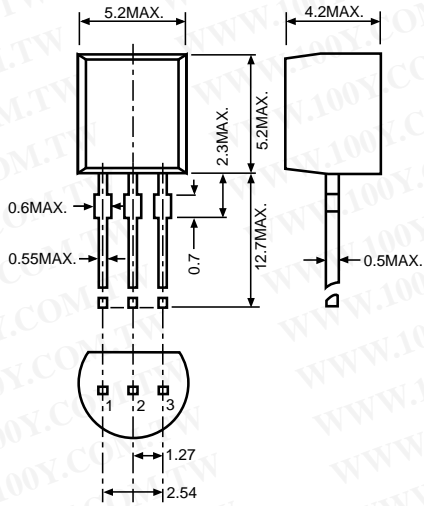
FIG. 9

1. When R5VL×C (CMOS Output) is used in FIG. 8, this IC may oscillate by the through-type current at the detection when impedance is connected between Power Source VDD and R5VL VDD Pin. When R5VL×A (Nch Open Drain Output) is used in FIG. 8, and R becomes excessively large, Detector Threshold may be varied because of the voltage drop of the supply current in the IC itself.
2. The connection as shown in FIG. 9 may cause the oscillation in both R5VL×C (CMOS Output) and R5VL×A (Nch Open Drain Output).

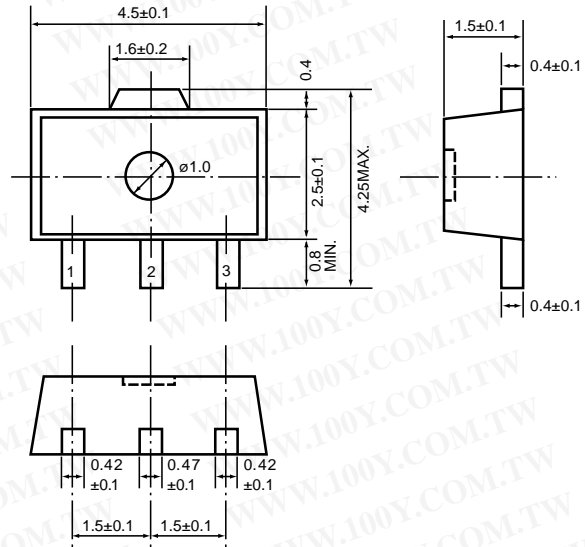
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PACKAGE DIMENSIONS (Unit: mm)

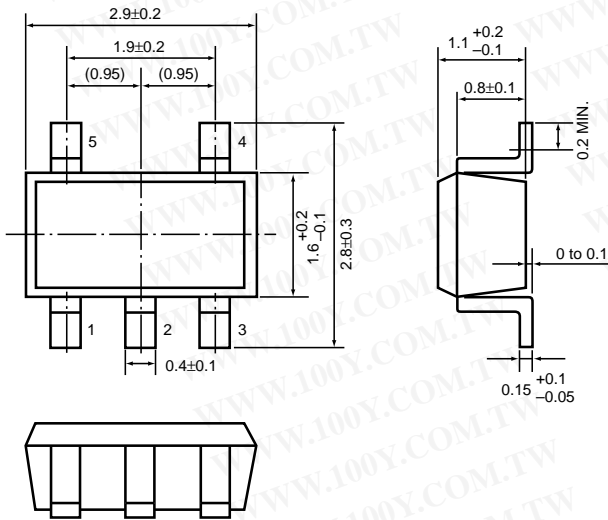
• TO-92



• SOT-89



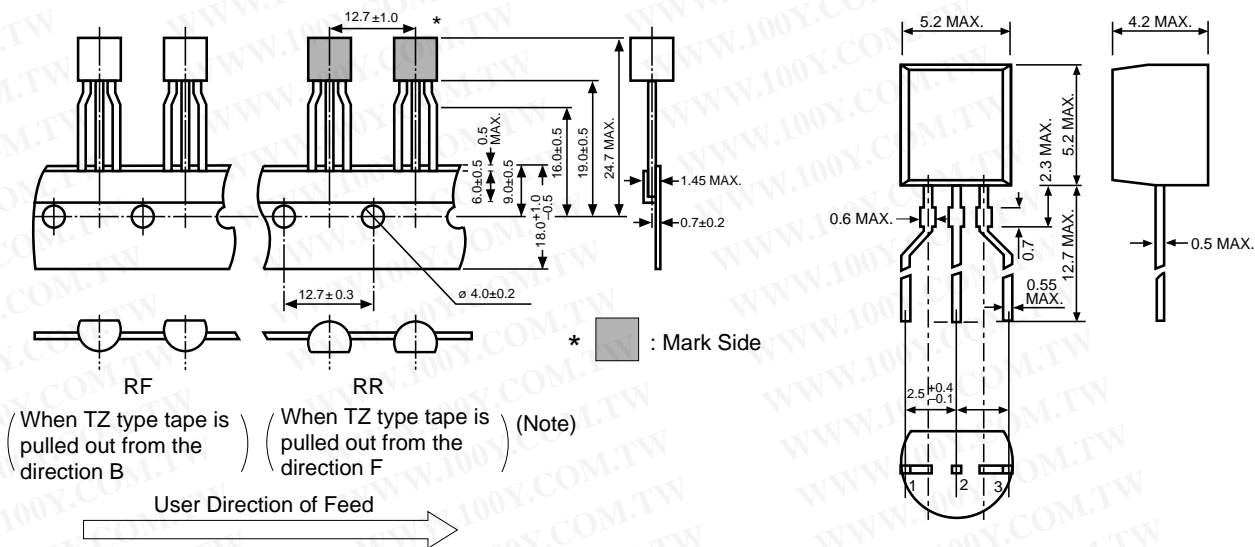
• SOT-23-5



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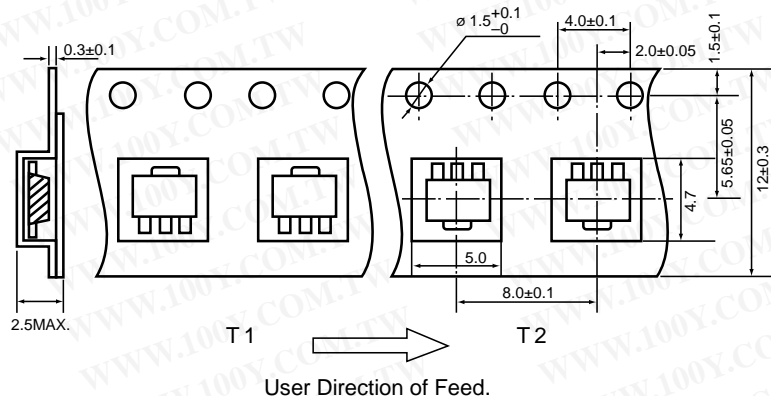
TAPING SPECIFICATIONS (Unit: mm)

• TO-92



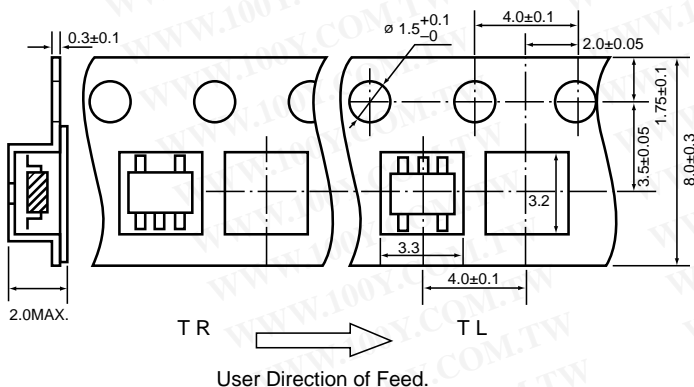
(Note) When taping is conducted, the pins of TO-92 are subjected to a particular forming.

• SOT-89



(Note) TZ type tape is not in the form of a reel, but is packed in a zigzag state in a box. Therefore, the tape can be used as either an RF type tape or an RR type tape, depending upon the pulling out direction (B or F).

• SOT-23-5



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