

LMG-SS24A12	240 DOTS x 128 DOTS	1/128 DUTY	1/12 BIAS
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1.FEATURE

LCD TYPE	STN/FSTN
LCM BACKLIGHT TYPE	EL/FL /LED BACKLIGHT
LCM CONTROLLER IC	BUILT IN LC7981 OR EQUIVALENT
POWER SUPPLY FOR LCM	DC +5.0V
LED BACKLIGHT INPUT	DC +5.0V
EL BACKLIGHT INPUT	AC 90~100V(400~700HZ)
EL INVERTER	SDEC-I003A
FL BACKLIGHT INPUT	AC 1KV(30KHZ)
FL INVERTER	SDEC-FL001(5A)
LCM DIMENSION	144.0 \ominus 104.0 \ominus 10.5(13.0) mm
LCM VIEWING AREA	114.0 \ominus 61.0 mm
LCD DOT SIZE	0.40 \ominus 0.40 mm
LCD DOT PITCH	0.44 \ominus 0.44 mm

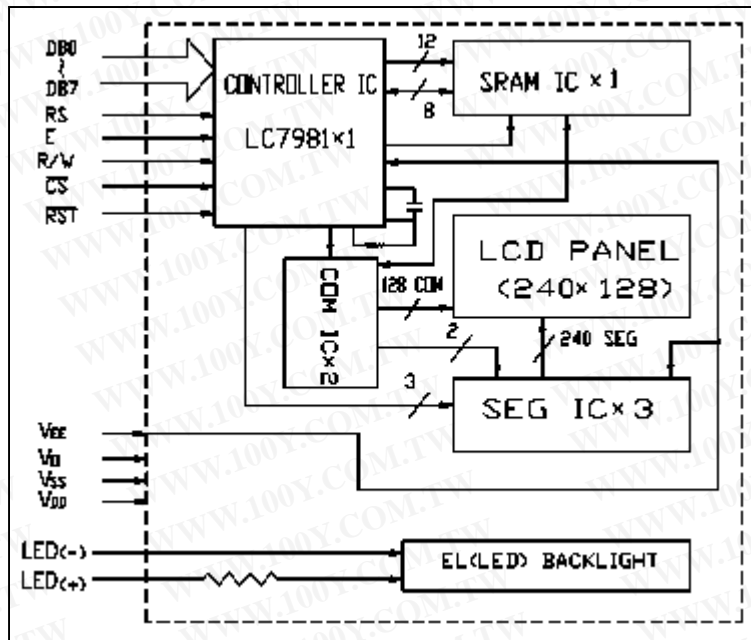
2.ELECTRICAL CHARACTERISTICS

ITEM	SYM	CONDITION	MIN	TYP	MAX	UNIT
SUPPLY VOLTAGE FOR LOGIC	VDD-VSS	Ta=25	4.5	5.0	5.5	V
SUPPLY VOLTAGE FOR LCD DRIVER	VDD-VSS	Ta=25	-	-	-5.0	V
OPERATING VOL. FOR LCD MODULE	VDD-VO	Ta=25	-	13.0	-	V
INPUT HIGH VOL	VIH	-	0.7VDD	-	VDD	V
INPUT LOW VOL.	VIL	-	0	-	0.3VDD	V
SUPPLY CURRENT FOR LOGIC	IDD	VDD=5.0V	-	-	15.0	V
SUPPLY CURRENT FOR LCD	ILCD	VO=-3.0V	-	-	20.0	mA
LED CURREN	IF	Ta=25	-	400	-	mA
LED DISSIPATION	PD	Ta=25	-	2000	-	mW

3.ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	MIN	TYP	MAX	UNIT
OPERATING TEMP	TOP	-10	-	+70	
STORAGE TEMP.	TST	-20	-	+80	
INPUT VOLTAGE	VI	VSS	-	VDD	V
SUPPLY VOL-LOGIC	VDD-VSS	-	-	7.0	V
SUPPLY VOL-LCD	VDD-VEE	15.0	-	-	V

4.BLOCK DIAGRAM

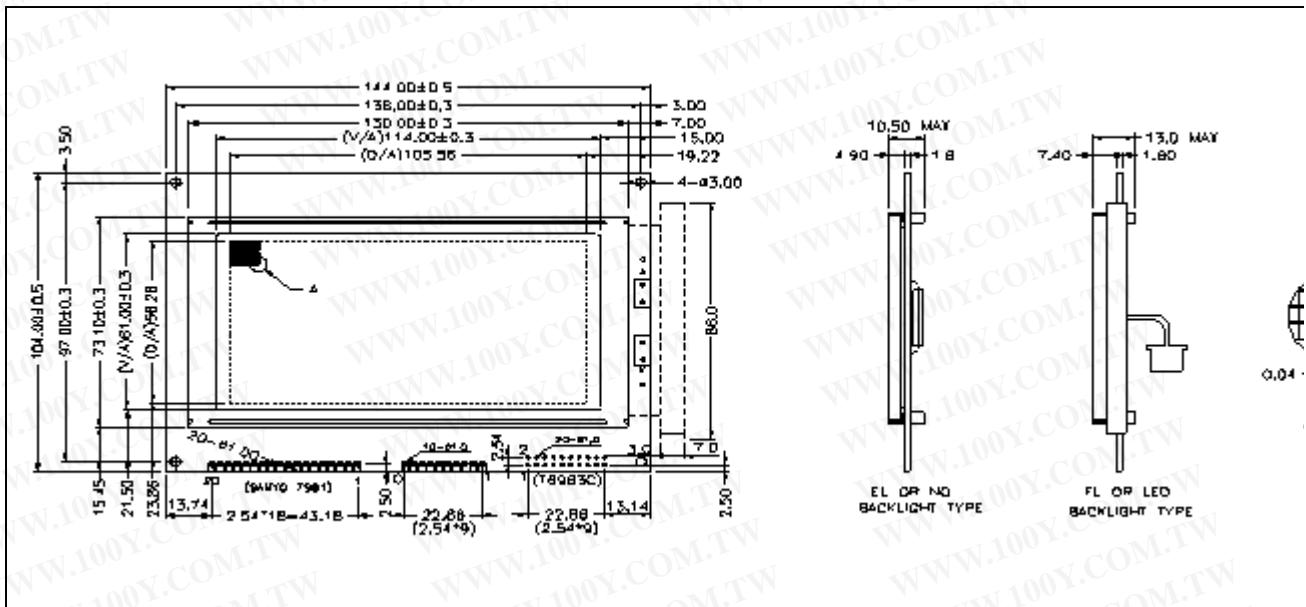


5.INTERFACE PIN CONNECTIONS

NO	SYM	LEVEL	FUNCTION
1	VSS	-	0V
2	VDD	-	+5V
3	VO	-	CONTRAST ADJ.
4	D/I	H/L	H : DATA L : INSTRUCTION CODE
5	R/W	H/L	H:READ(LCD -> MPU) L:WRITE(MPU ->LCD)
6	E	H.H->L	ENABLE SIGNAL
7	DB0	H/L	DATA BIT0
8	DB1	H/L	DATA BIT1
9	DB2	H/L	DATA BIT2
10	DB3	H/L	DATA BIT3
11	DB4	H/L	DATA BIT4
12	DB5	H/L	DATA BIT5
13	DB6	H/L	DATA BIT6
14	DB7	H/L	DATA BIT7
15	CS	H/L	CHIP SELECT SIGNAL
16	RST	L	RESET SIGNAL
17	VEE	-	NEGATIVE VOLTAGE OUTPUT (-5.0V)
18	N.C	-	NO CONNECTION
19	A(+)	+5.0V	BACKLIGHT(+)
20	K(-)	0V	BACKLIGHT(-)

6.DIMENSIONAL DRAWING

勝特力材料 886-3-5753170
 勝特力电子(上海) 86-21-54151736
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[Http://www.100y.com.tw](http://www.100y.com.tw)



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