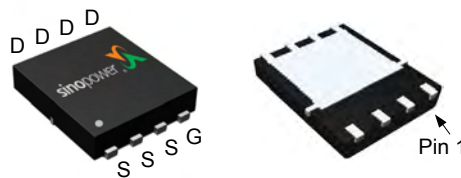


N-Channel Enhancement Mode MOSFET

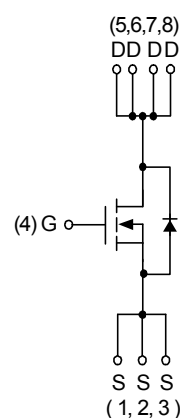
Features

- 30V/60A,
 $R_{DS(ON)} = 5.7m\Omega$ (Max.) @ $V_{GS} = 10V$
 $R_{DS(ON)} = 7.6m\Omega$ (Max.) @ $V_{GS} = 4.5V$
- 100% UIS + R_g Tested
- Reliable and Rugged
- Lead Free and Green Devices Available
(RoHS Compliant)

Pin Description



DFN5x6A-8_EP



N-Channel MOSFET

Applications

- Power Management in Desktop Computer or DC/DC Converters.

Ordering and Marking Information

SM4364NA □□□-□□□ Assembly Material Handling Code Temperature Range Package Code	Package Code KP : DFN5x6A-8_EP Operating Junction Temperature Range C : -55 to 150 °C Handling Code TR : Tape & Reel Assembly Material G : Halogen and Lead Free Device
SM4364NA KP : 	XXXXXX - Lot Code

Note : SINOPOWER lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. SINOPOWER lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020D for MSL classification at lead-free peak reflow temperature. SINOPOWER defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

SINOPOWER reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter		Rating	Unit
Common Ratings				
V _{DSS}	Drain-Source Voltage		30	V
V _{GSS}	Gate-Source Voltage		±20	
T _J	Maximum Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-55 to 150	°C
I _S	Diode Continuous Forward Current		20	A
I _{DP}	Pulse Drain Current Tested	T _C =25°C	140	A
		T _C =100°C	90	
I _D ^a	Continuous Drain Current	T _C =25°C	60*	A
		T _C =100°C	48	
P _D	Maximum Power Dissipation	T _C =25°C	50	W
		T _C =100°C	20	
R _{θJC}	Thermal Resistance-Junction to Case	Steady State	2.5	°C/W
R _{θJA} ^b	Thermal Resistance-Junction to Ambient	t ≤ 10s	26	°C/W
		Steady State	70	
I _{AS} ^c	Avalanche Current, Single pulse	L=0.1mH	30	A
		L=0.3mH	22	
		L=0.5mH	20	
E _{AS} ^c	Avalanche Energy, Single pulse	L=0.1mH	45	mJ
		L=0.3mH	72	
		L=0.5mH	100	

Note a : * Current limited by bond wire.

Note b : Surface Mounted on 1in^2 pad area, $t = 999\text{sec}$.

Note c : UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature $T_J = 25^\circ\text{C}$).

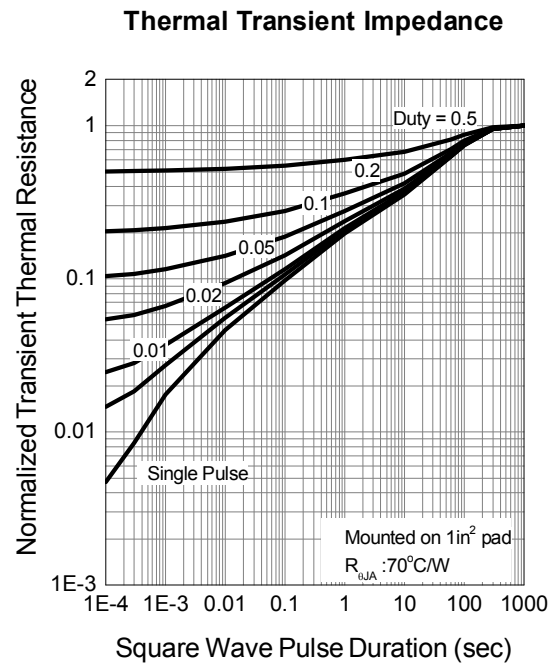
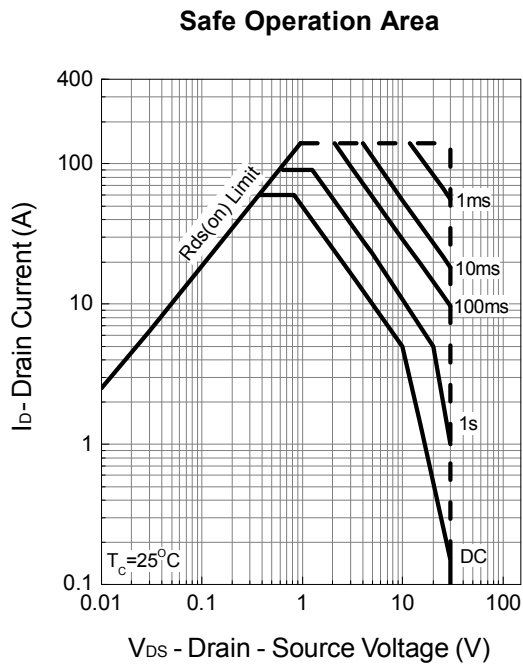
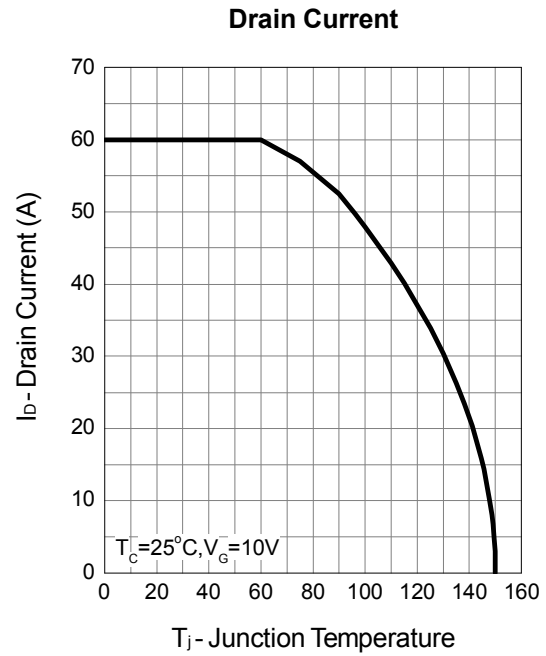
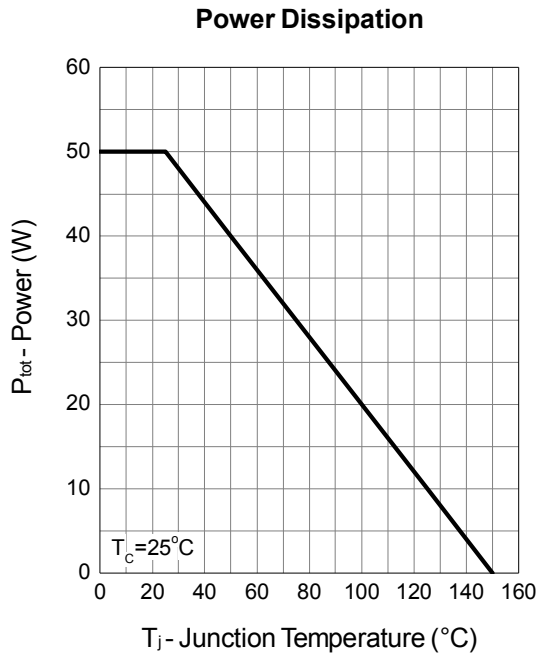
Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	30	-	-	V
BV _{DSS} t	Drain-Source Breakdown Voltage (transient)	V _{GS} =0V, I _{D(aval)} =20A T _{case} =25°C, t _{transient} =100ns	34	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =24V, V _{GS} =0V T _J =85°C	- -	- -	1 30	μA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	1.5	1.8	2.5	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
R _{DS(ON)} ^d	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =40A T _J =125°C V _{GS} =4.5V, I _{DS} =20A	- - -	4.7 6.9 5.8	5.7 - 7.6	mΩ
G _{fs}	Forward Transconductance	V _{DS} =5V, I _{DS} =40A	-	95	-	S
Diode Characteristics						
V _{SD} ^d	Diode Forward Voltage	I _{SD} =20A, V _{GS} =0V	-	0.8	1.1	V
t _{rr}	Reverse Recovery Time	I _{DS} =40A, dI _{SD} /dt=100A/μs	-	21	-	ns
t _a	Charge Time		-	13.2	-	
t _b	Discharge Time		-	7.8	-	
Q _{rr}	Reverse Recovery Charge		-	7	-	nC
Dynamic Characteristics ^e						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	-	3	4.5	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =15V, Frequency=1.0MHz	1200	1500	1820	pF
C _{oss}	Output Capacitance		210	260	310	
C _{rss}	Reverse Transfer Capacitance		100	130	190	
t _{d(ON)}	Turn-on Delay Time	V _{DD} =15V, R _L =15Ω, I _{DS} =1A, V _{GEN} =10V, R _G =6Ω	-	15	28	ns
t _r	Turn-on Rise Time		-	13	24	
t _{d(OFF)}	Turn-off Delay Time		-	32	57	
t _f	Turn-off Fall Time		-	9	17	
Gate Charge Characteristics ^e						
Q _g	Total Gate Charge	V _{DS} =15V, V _{GS} =10V, I _{DS} =40A	-	25	37	nC
Q _g	Total Gate Charge	V _{DS} =15V, V _{GS} =4.5V, I _{DS} =40A	-	12	17	
Q _{gth}	Threshold Gate Charge		-	1.5	-	
Q _{gs}	Gate-Source Charge		-	3	-	
Q _{gd}	Gate-Drain Charge		-	7.5	-	

Note d : Pulse test ; pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

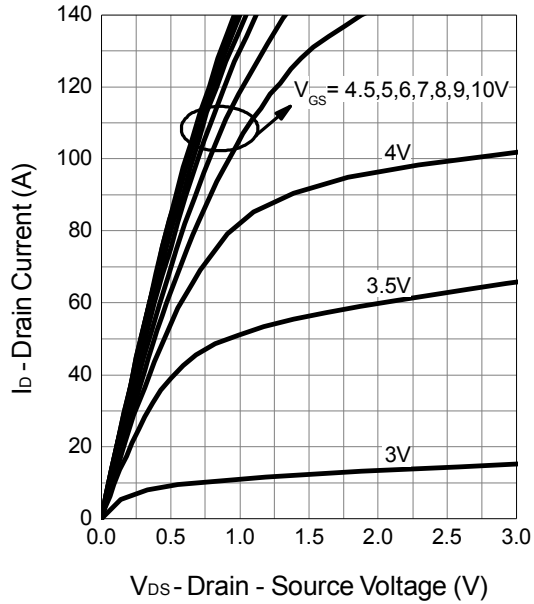
Note e : Guaranteed by design, not subject to production testing.

Typical Operating Characteristics

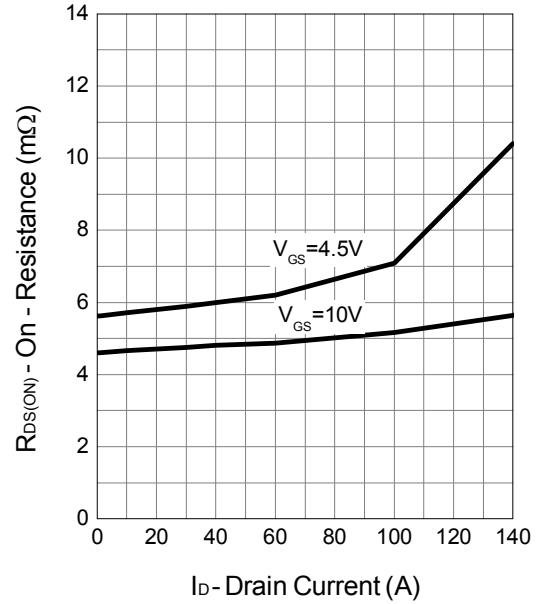


Typical Operating Characteristics (Cont.)

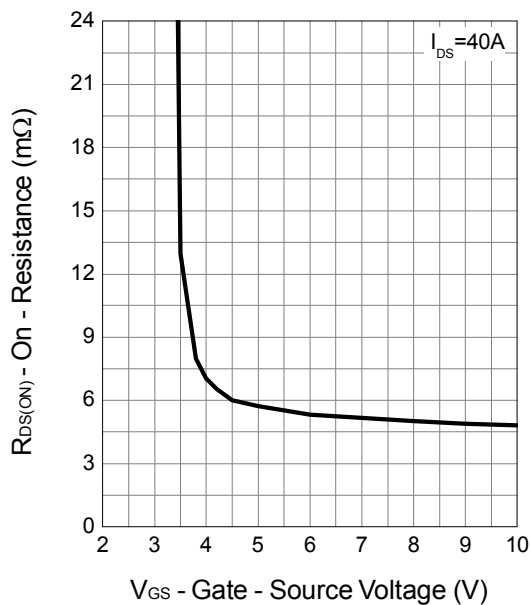
Output Characteristics



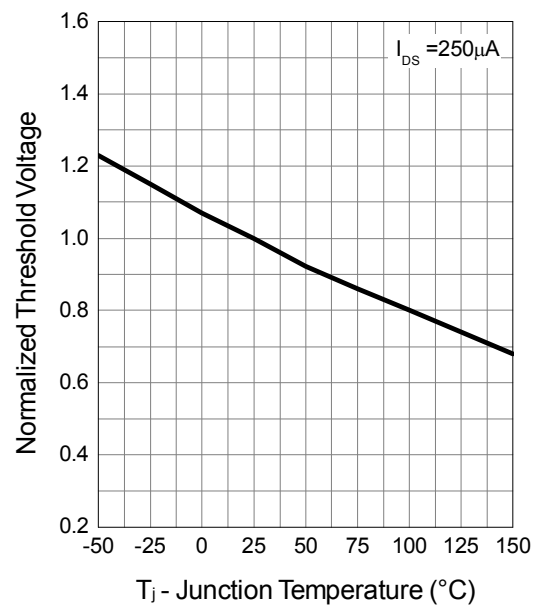
Drain-Source On Resistance



Gate-Source On Resistance

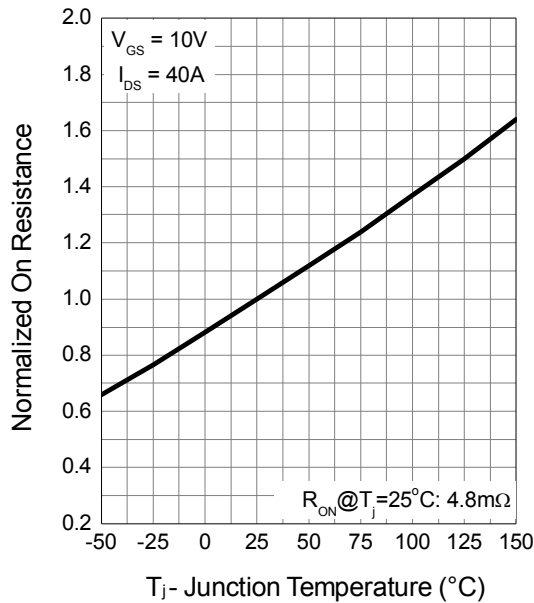


Gate Threshold Voltage

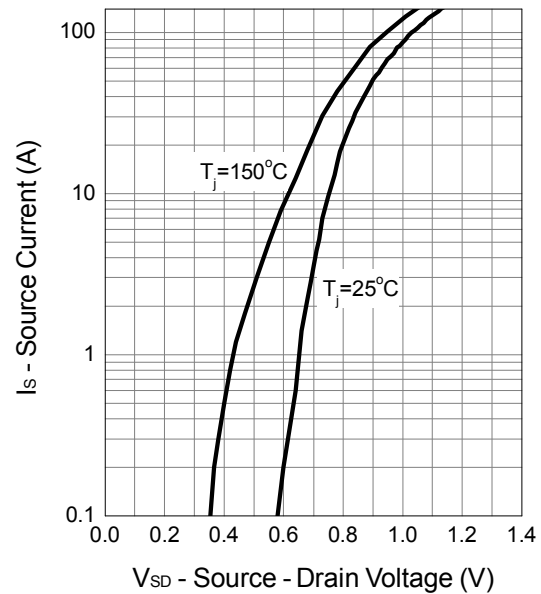


Typical Operating Characteristics (Cont.)

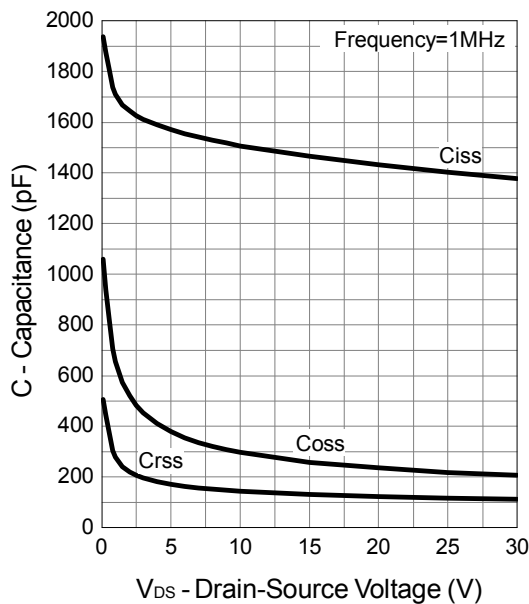
Drain-Source On Resistance



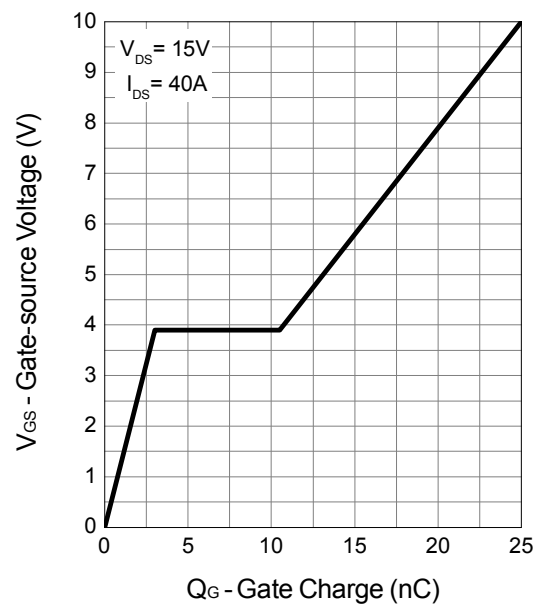
Source-Drain Diode Forward



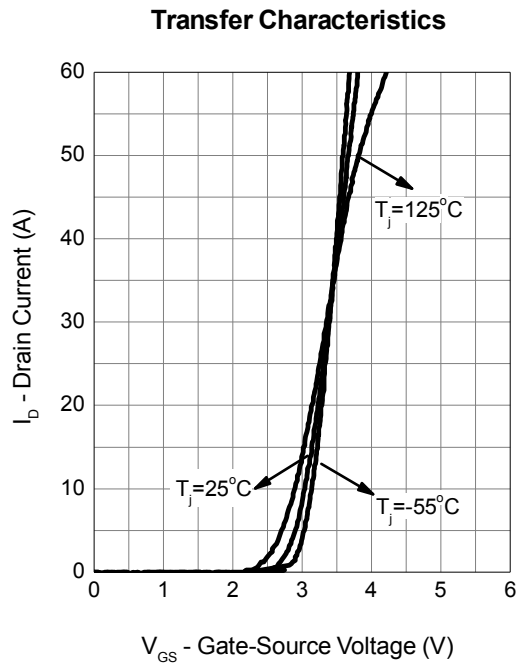
Capacitance



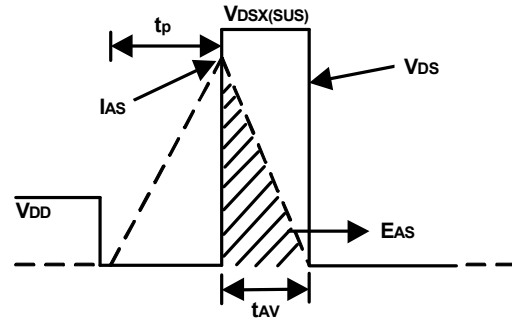
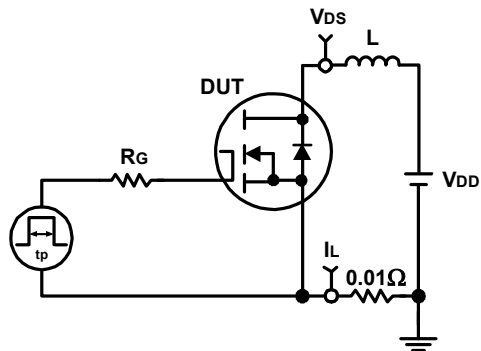
Gate Charge



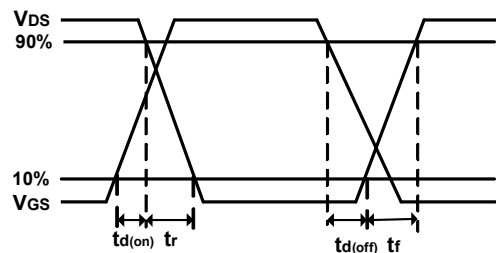
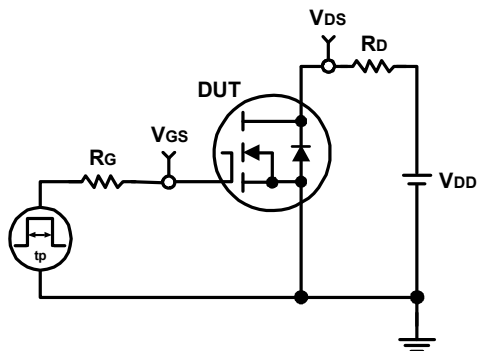
Typical Operating Characteristics (Cont.)



Avalanche Test Circuit and Waveforms



Switching Time Test Circuit and Waveforms



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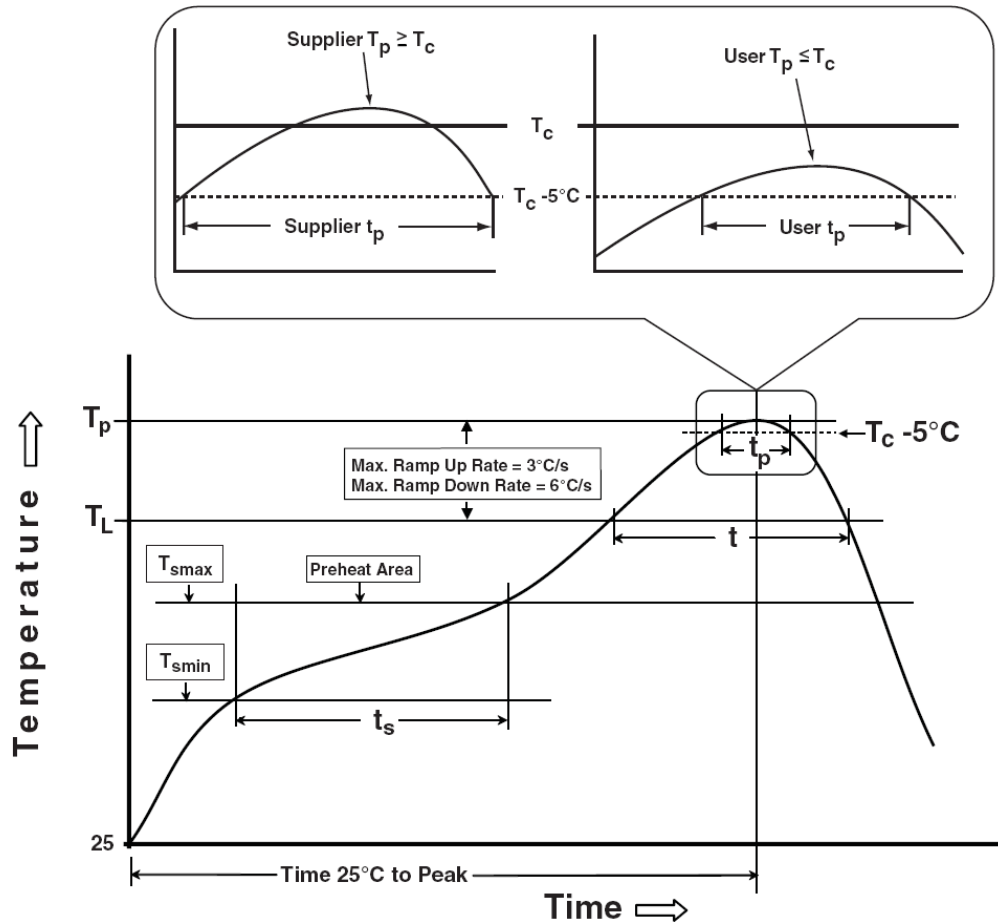
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Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak Temperature min (T_{smin}) Temperature max (T_{smax}) Time (T_{smin} to T_{smax}) (t_s)	100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-120 seconds
Average ramp-up rate (T_{smax} to T_p)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L) Time at liquidous (t_L)	183 °C 60-150 seconds	217 °C 60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_p)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum. ** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.		

Table 1. SnPb Eutectic Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	1000 Hrs, 80% of VDS max @ T_{jmax}
HTGB	JESD-22, A108	1000 Hrs, 100% of VGS max @ T_{jmax}
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -65°C~150°C

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