

Transistors

# 4V Drive Nch MOS FET

## RSQ035N03

●Structure

Silicon N-channel MOS FET

●Features

- 1) Low On-resistance.
- 2) Space saving, small surface mount package (TSMT6).

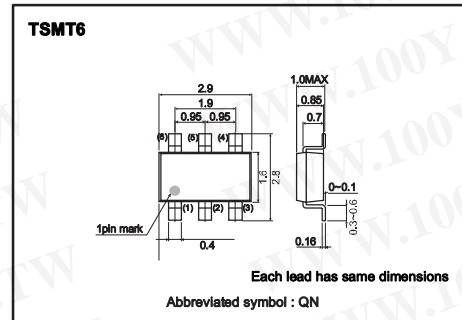
●Applications

Switching

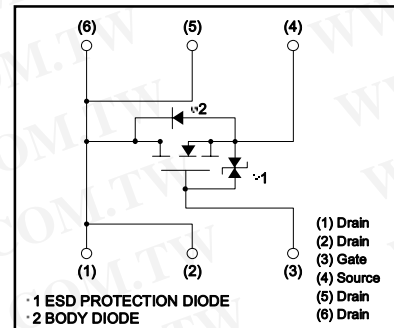
●Packaging specifications

Type	Package	Taping
	Code	TR
	Basic ordering unit (pieces)	3000
RSQ035N03		○

●External dimensions (Unit : mm)



●Inner circuit



●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
Drain-source voltage	V <sub>DS</sub>	30	V	
Gate-source voltage	V <sub>GS</sub>	20	V	
Drain current	Continuous	I <sub>D</sub>	±3.5	A
	Pulsed	I <sub>DP</sub> <sup>*1</sup>	±14	A
Source current (Body diode)	Continuous	I <sub>S</sub>	1.0	A
	Pulsed	I <sub>SP</sub> <sup>*1</sup>	14	A
Total power dissipation	P <sub>D</sub> <sup>*2</sup>	1.25	W	
Channel temperature	T <sub>ch</sub>	150	°C	
Range of storage temperature	T <sub>stg</sub>	-55 to +150	°C	

<sup>\*1</sup> P<sub>w</sub>:10μs, Duty cycle:1%

<sup>\*2</sup> Mounted on a ceramic board

●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	R <sub>th(ch-a)</sub> <sup>†</sup>	100	°C/W

<sup>†</sup> Mounted on a ceramic board

## Transistors

## ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-source leakage	I <sub>gss</sub>	–	–	10	μA	V <sub>GS</sub> =20V, V <sub>DS</sub> =0V
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	30	–	–	V	I <sub>D</sub> = 1mA, V <sub>GS</sub> =0V
Zero gate voltage drain current	I <sub>DSS</sub>	–	–	1	μA	V <sub>DS</sub> = 30V, V <sub>GS</sub> =0V
Gate threshold voltage	V <sub>GS(th)</sub>	1.0	–	2.5	V	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA
Static drain-source on-state resistance	R <sub>DS(on)</sub>	–	44	62	mΩ	I <sub>D</sub> = 3.5A, V <sub>GS</sub> = 10V
		–	60	84	mΩ	I <sub>D</sub> = 3.5A, V <sub>GS</sub> = 4.5V
		–	67	94	mΩ	I <sub>D</sub> = 3.5A, V <sub>GS</sub> = 4V
Forward transfer admittance	Y <sub>fs</sub>	2.0	–	–	S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 3.5A
Input capacitance	C <sub>iss</sub>	–	290	–	pF	V <sub>DS</sub> = 10V
Output capacitance	C <sub>oss</sub>	–	85	–	pF	V <sub>GS</sub> =0V
Reverse transfer capacitance	C <sub>rss</sub>	–	55	–	pF	f=1MHz
Turn-on delay time	t <sub>d(on)</sub>	–	7	–	ns	V <sub>DD</sub> = 15V I <sub>D</sub> = 1.75A
Rise time	t <sub>r</sub>	–	9	–	ns	V <sub>GS</sub> = 10V
Turn-off delay time	t <sub>d(off)</sub>	–	24	–	ns	R <sub>L</sub> =8.57Ω
Fall time	t <sub>f</sub>	–	6	–	ns	R <sub>G</sub> =10Ω
Total gate charge	Q <sub>g</sub>	–	5.3	7.4	nC	V <sub>DD</sub> = 15V V <sub>GS</sub> = 5V
Gate-source charge	Q <sub>gs</sub>	–	1.0	–	nC	I <sub>D</sub> = 3.5A
Gate-drain charge	Q <sub>gd</sub>	–	1.4	–	nC	R <sub>L</sub> = 4.29Ω R <sub>G</sub> =10Ω

Pulsed

## ●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V <sub>SD</sub>	–	–	1.2	V	I <sub>S</sub> = 1.0A, V <sub>GS</sub> =0V

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 勝特力电子(深圳) 86-755-83298787  
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