FAIRCHILD SEMICONDUCTOR®

RHRG5060

Data Sheet **January 2002**

50A, 600V Hyperfast Diode

The RHRG5060 is a hyperfast diode with soft recovery characteristics (t_{rr} < 45ns). It has half the recovery time of ultrafast diodes and is of silicon nitride passivated ion-implanted epitaxial planar construction.

This device is intended for use as a freewheeling/clamping diode and rectifier in a variety of switching power supplies and other power switching applications. Its low stored charge and hyperfast soft recovery minimize ringing and electrical noise in many power switching circuits, thus reducing power loss in the switching transistors.

Formerly developmental type TA49065.

Ordering Information

PART NUMBER	PACKAGE	BRAND
RHRG5060	TO-247	RHRG5060

NOTE: When ordering, use the entire part number.

Symbol

Features

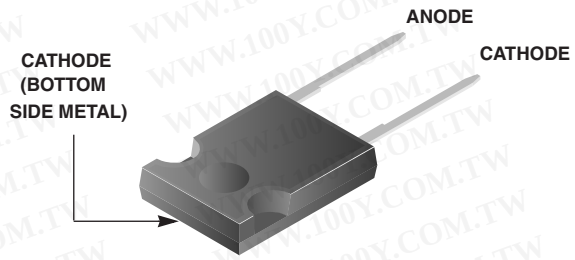
- Hyperfast with Soft Recovery . . . . . <45ns
- Operating Temperature . . . . . 175°C
- Reverse Voltage 600V
- Avalanche Energy Rated
- Planar Construction

Applications

- Switching Power Supplies
- Power Switching Circuits
- General Purpose

Packaging

JEDEC STYLE TO-247



Absolute Maximum Ratings $T_C = 25^oC$, Unless Otherwise Specified

		RHRG5060	UNITS
Peak Repetitive Reverse Voltage	V_{RRM}	600	V
Working Peak Reverse Voltage	V_{RWM}	600	V
DC Blocking Voltage	V_R	600	V
Average Rectified Forward Current ($T_C = 93^oC$)	$I_{F(AV)}$	50	A
Repetitive Peak Surge Current (Square Wave, 20kHz)	I_{FRM}	100	A
Nonrepetitive Peak Surge Current (Halfwave, 1 Phase, 60Hz)	I_{FSM}	500	A
Maximum Power Dissipation	P_D	150	W
Avalanche Energy (See Figures 10 and 11)	E_{AVL}	40	mJ
Operating and Storage Temperature	T_{STG}, T_J	-65 to 175	°C

Electrical Specifications $T_C = 25^oC$, Unless Otherwise Specified

SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
V_F	$I_F = 50A$	-	-	2.1	V
	$I_F = 50A$, $T_C = 150^oC$	-	-	1.7	V
I_R	$V_R = 600V$	-	-	250	μA
	$V_R = 600V$, $T_C = 150^oC$	-	-	1.5	mA
t_{rr}	$I_F = 1A$, $dI_F/dt = 100A/\mu s$	-	-	45	ns
	$I_F = 50A$, $dI_F/dt = 100A/\mu s$	-	-	50	ns
t_a	$I_F = 50A$, $dI_F/dt = 100A/\mu s$	-	25	-	ns
t_b	$I_F = 50A$, $dI_F/dt = 100A/\mu s$	-	20	-	ns
Q_{RR}	$I_F = 50A$, $dI_F/dt = 100A/\mu s$	-	65	-	nC
C_J	$V_R = 10V$, $I_F = 0A$	-	140	-	pF
$R_{\theta JC}$		-	-	1.0	$^oC/W$

DEFINITIONS

V_F = Instantaneous forward voltage (pw = 300μs, D = 2%).

I_R = Instantaneous reverse current.

t_{rr} = Reverse recovery time (See Figure 9), summation of $t_a + t_b$.

t_a = Time to reach peak reverse current (See Figure 9).

t_b = Time from peak I_{RM} to projected zero crossing of I_{RM} based on a straight line from peak I_{RM} through 25% of I_{RM} (See Figure 9).

Q_{RR} = Reverse recovery charge.

C_J = Junction Capacitance.

$R_{\theta JC}$ = Thermal resistance junction to case.

pw = pulse width.

D = Duty cycle.

Typical Performance Curves

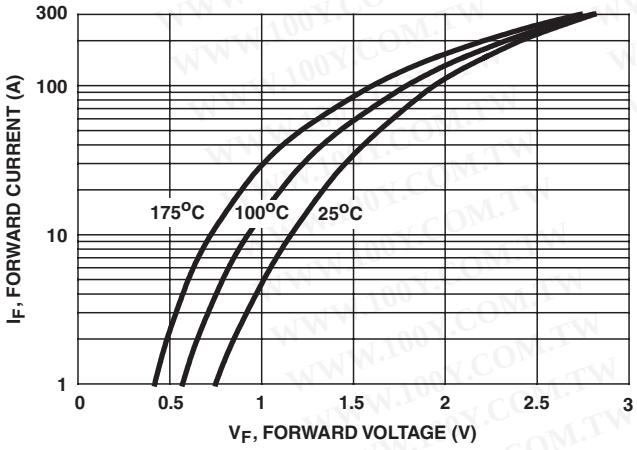


FIGURE 1. FORWARD CURRENT vs FORWARD VOLTAGE

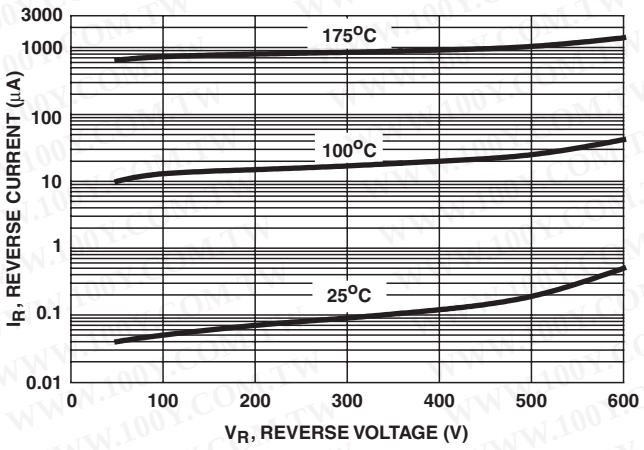


FIGURE 2. REVERSE CURRENT vs REVERSE VOLTAGE

Typical Performance Curves (Continued)

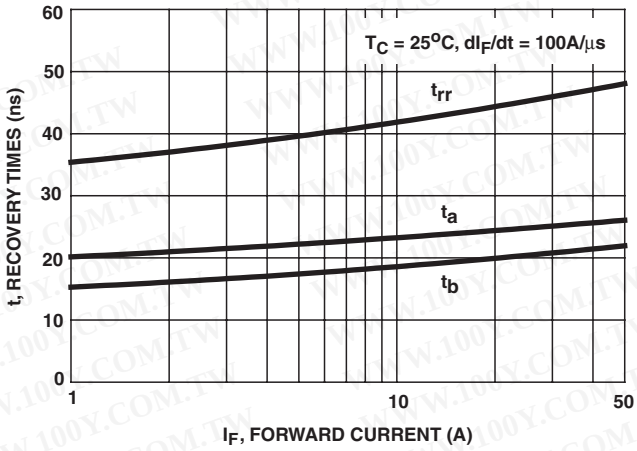


FIGURE 3. t_{rr}, t_a AND t_b CURVES vs FORWARD CURRENT

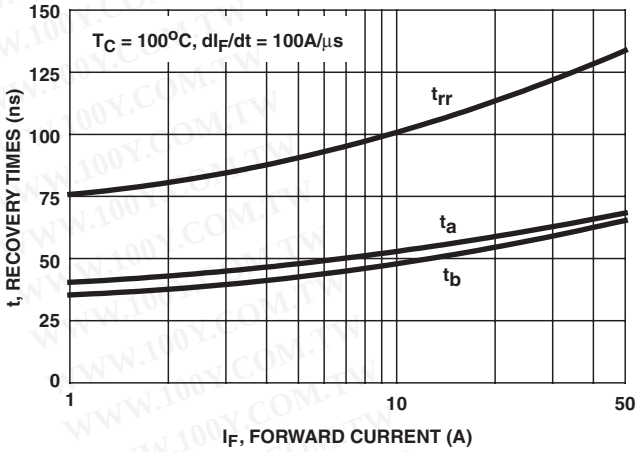


FIGURE 4. t_{rr}, t_a AND t_b CURVES vs FORWARD CURRENT

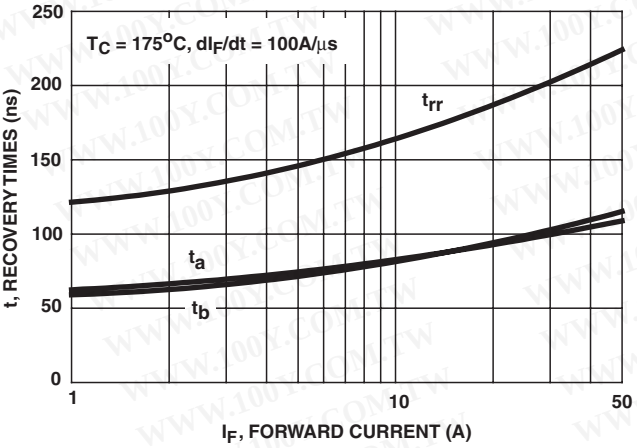


FIGURE 5. t_{rr}, t_a AND t_b CURVES vs FORWARD CURRENT

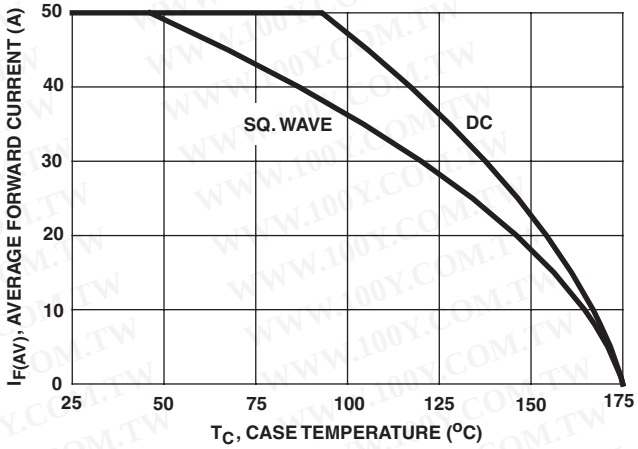


FIGURE 6. CURRENT DERATING CURVE

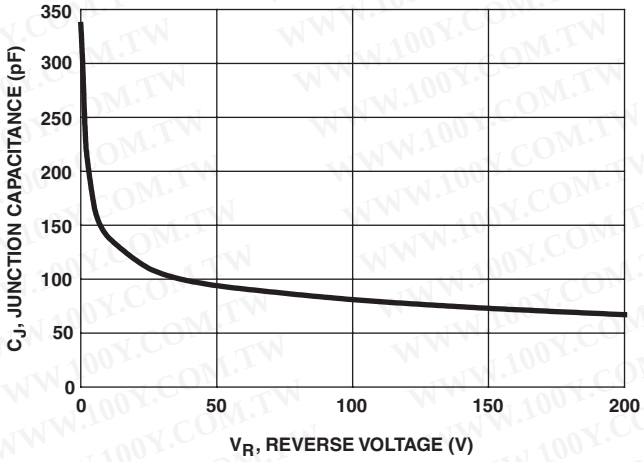


FIGURE 7. JUNCTION CAPACITANCE vs REVERSE VOLTAGE

Test Circuits and Waveforms

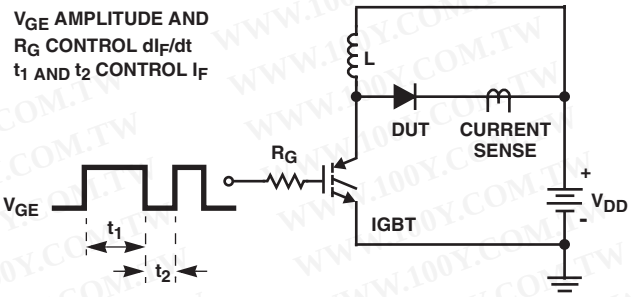


FIGURE 8. t_{rr} TEST CIRCUIT

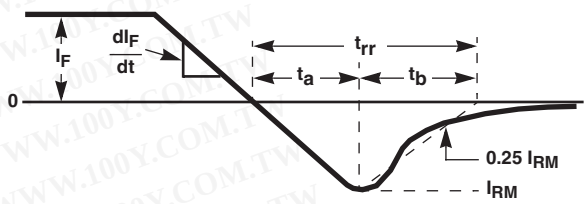


FIGURE 9. t_{rr} WAVEFORMS AND DEFINITIONS

I_{MAX} = 1.4A
L = 40mH
R < 0.1Ω
$E_{AVL} = 1/2LI^2 [V_{R(AVL)}/(V_{R(AVL)} - V_{DD})]$
Q_1 = IGBT (BV_{CES} > DUT $V_{R(AVL)}$)

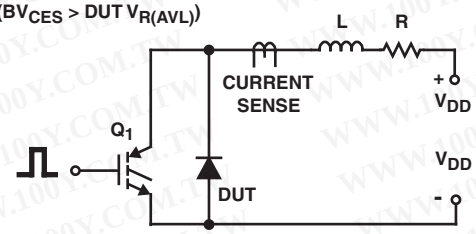


FIGURE 10. AVALANCHE ENERGY TEST CIRCUIT

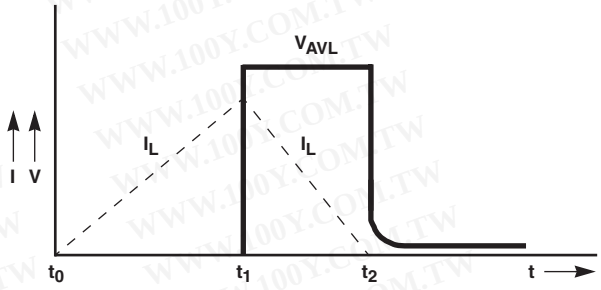


FIGURE 11. AVALANCHE CURRENT AND VOLTAGE WAVEFORMS