

FDB8832

N-Channel Logic Level PowerTrench® MOSFET

30V, 80A, 2.1mΩ

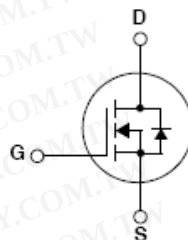
Features

- Typ $r_{DS(on)}$ = 1.5mΩ at V_{GS} = 5V, I_D = 80A
- Typ $Q_{g(5)}$ = 100nC at V_{GS} = 5V
- Low Miller Charge
- Low Q_{rr} Body Diode
- UIS Capability (Single Pulse and Repetitive Pulse)
- Qualified to AEC Q101
- RoHS Compliant



Applications

- 12V Automotive Load Control
- Starter / Alternator Systems
- Electronic Power Steering Systems
- ABS
- DC-DC Converters



MOSFET Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Ratings	Units
V_{DS}	Drain to Source Voltage	30	V
V_{GS}	Gate to Source Voltage	± 20	V
I_D	Drain Current Continuous ($T_C < 165^\circ\text{C}$, $V_{GS} = 10\text{V}$)	80	A
	Drain Current Continuous ($T_C < 163^\circ\text{C}$, $V_{GS} = 5\text{V}$)	80	
	Drain Current Continuous ($T_{amb} = 25^\circ\text{C}$, $V_{GS} = 10\text{V}$, with $R_{\theta JA} = 43^\circ\text{C/W}$)	34	
	Pulsed	See Figure 4	
E_{AS}	Single Pulse Avalanche Energy (Note 1)	1246	mJ
P_D	Power Dissipation	300	W
	Derate above 25°C	2	W/ $^\circ\text{C}$
T_J, T_{STG}	Operating and Storage Temperature	-55 to +175	$^\circ\text{C}$

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction to Case	0.5	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note 2)	62	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, 1in^2 copper pad area	43	$^\circ\text{C/W}$

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDB8832	FDB8832	TO-263AB	330mm	24mm	800 units

Electrical Characteristics $T_J = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
--------	-----------	-----------------	-----	-----	-----	-------

Off Characteristics

$B_{V_{DS}}$	Drain to Source Breakdown Voltage	$I_D = 250\mu\text{A}$, $V_{GS} = 0\text{V}$	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 24\text{V}$ $V_{GS} = 0\text{V}$ $T_J = 150^\circ\text{C}$	-	-	1	μA
I_{GSS}	Gate to Source Leakage Current	$V_{GS} = \pm 20\text{V}$	-	-	± 100	nA

On Characteristics

$V_{GS(th)}$	Gate to Source Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 250\mu\text{A}$	1.0	1.6	3.0	V
$r_{DS(on)}$	Drain to Source On Resistance	$I_D = 80\text{A}$, $V_{GS} = 10\text{V}$	-	1.4	1.9	$\text{m}\Omega$
		$I_D = 80\text{A}$, $V_{GS} = 5\text{V}$	-	1.5	2.1	
		$I_D = 80\text{A}$, $V_{GS} = 4.5\text{V}$	-	1.6	2.2	
		$I_D = 80\text{A}$, $V_{GS} = 10\text{V}$	-	2.3	3.0	
		$T_J = 175^\circ\text{C}$	-			

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{DS} = 15\text{V}$, $V_{GS} = 0\text{V}$, $f = 1\text{MHz}$	-	11400	-	pF
C_{oss}	Output Capacitance		-	2140	-	pF
C_{rss}	Reverse Transfer Capacitance		-	1260	-	pF
R_G	Gate Resistance	$V_{GS} = 0.5\text{V}$, $f = 1\text{MHz}$	-	1.2	-	Ω
$Q_{g(TOT)}$	Total Gate Charge at 10V	$V_{GS} = 0$ to 10V	-	204	265	nC
$Q_{g(5)}$	Total Gate Charge at 5V	$V_{GS} = 0$ to 5V	-	100	130	nC
$Q_{g(TH)}$	Threshold Gate Charge	$V_{GS} = 0$ to 1V	-	10.9	14.2	nC
Q_{gs}	Gate to Source Gate Charge	$V_{DD} = 15\text{V}$ $I_D = 80\text{A}$ $I_g = 1.0\text{mA}$	-	33	-	nC
Q_{gs2}	Gate Charge Threshold to Plateau		-	22	-	nC
Q_{gd}	Gate to Drain "Miller" Charge		-	43	-	nC
			-			

Electrical Characteristics $T_J = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
--------	-----------	-----------------	-----	-----	-----	-------

Switching Characteristics

$t_{(on)}$	Turn-On Time	$V_{DD} = 15\text{V}, I_D = 80\text{A}$ $V_{GS} = 5\text{V}, R_{GS} = 1.5\Omega$	-	-	155	ns
$t_{d(on)}$	Turn-On Delay Time		-	24	-	ns
t_r	Turn-On Rise Time		-	73	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	54	-	ns
t_f	Turn-Off Fall Time		-	38	-	ns
t_{off}	Turn-Off Time		-	-	149	ns

Drain-Source Diode Characteristics

V_{SD}	Source to Drain Diode Voltage	$I_{SD} = 75\text{A}$	-	0.8	1.25	V
		$I_{SD} = 40\text{A}$	-	0.8	1.0	V
t_{rr}	Reverse Recovery Time	$I_F = 75\text{A}, di/dt = 100\text{A}/\mu\text{s}$	-	59	77	ns
Q_{rr}	Reverse Recovery Charge	$I_F = 75\text{A}, di/dt = 100\text{A}/\mu\text{s}$	-	67	87	nC

Notes:1: Starting $T_J = 25^\circ\text{C}$, $L = 0.61\text{mH}$, $I_{AS} = 64\text{A}$, $V_{DD} = 30\text{V}$, $V_{GS} = 10\text{V}$.

2: Pulse width = 100s.

This product has been designed to meet the extreme test conditions and environment demanded by the automotive industry. For a copy of the requirements, see AEC Q101 at: <http://www.aecouncil.com/>

All Fairchild Semiconductor products are manufactured, assembled and tested under ISO9000 and QS9000 quality systems certification.

Typical Characteristics

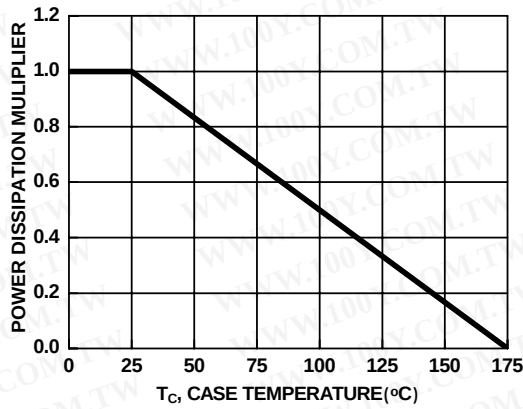


Figure 1. Normalized Power Dissipation vs Case Temperature

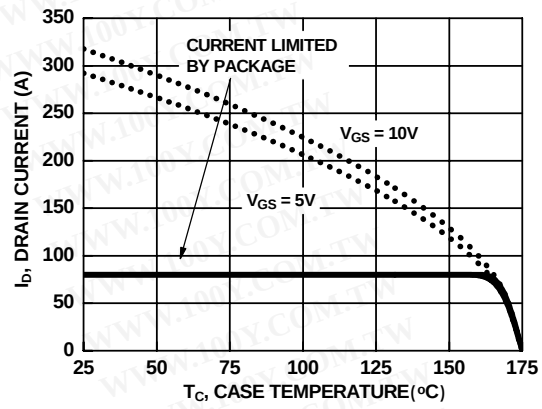


Figure 2. Maximum Continuous Drain Current vs Case Temperature

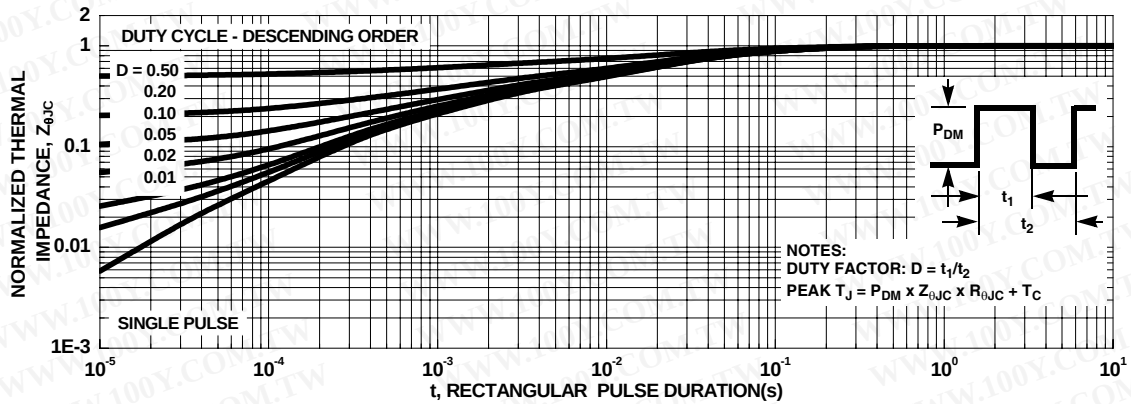


Figure 3. Normalized Maximum Transient Thermal Impedance

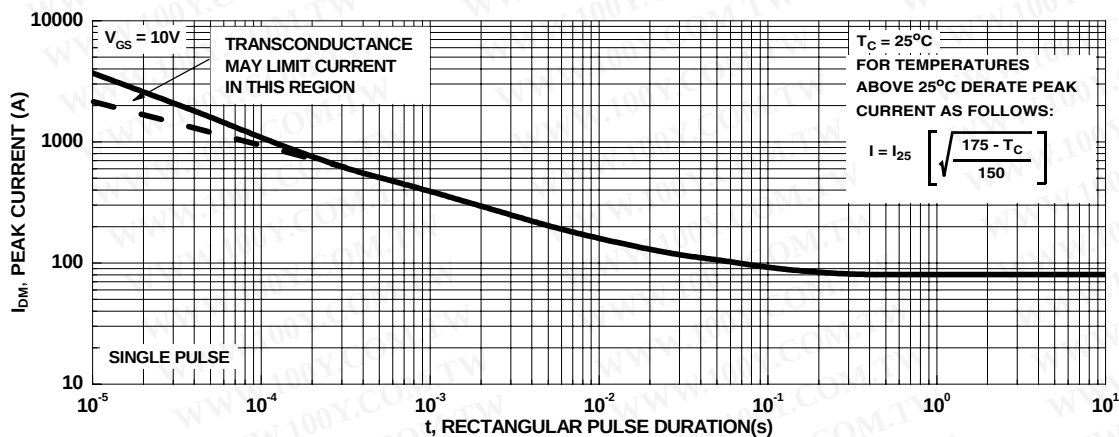


Figure 4. Peak Current Capability

Typical Characteristics

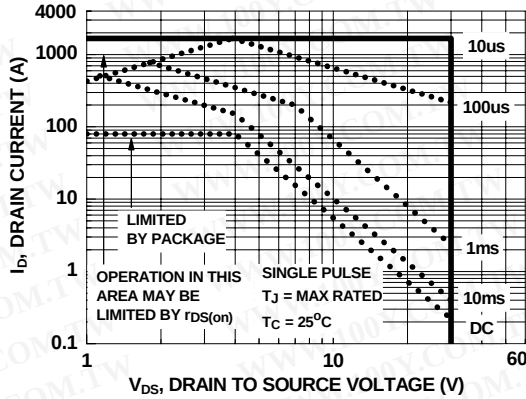
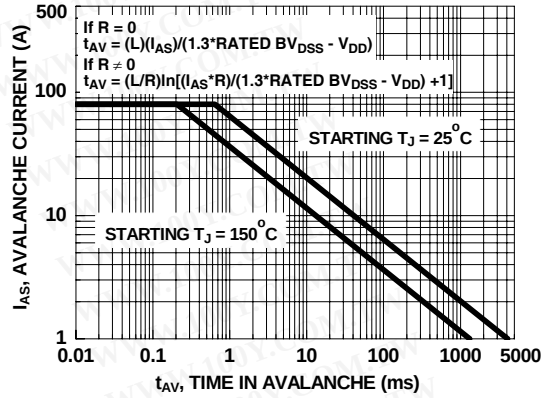


Figure 5. Forward Bias Safe Operating Area



NOTE: Refer to Fairchild Application Notes AN7514 and AN7515

Figure 6. Unclamped Inductive Switching Capability

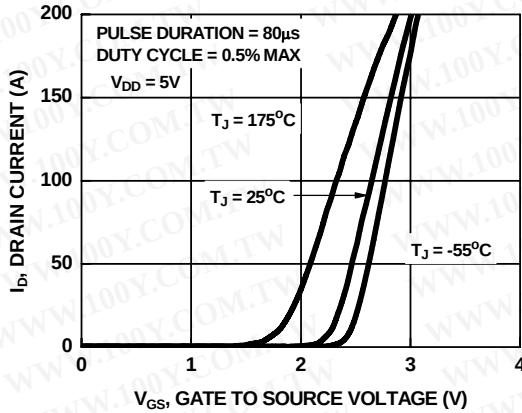


Figure 7. Transfer Characteristics

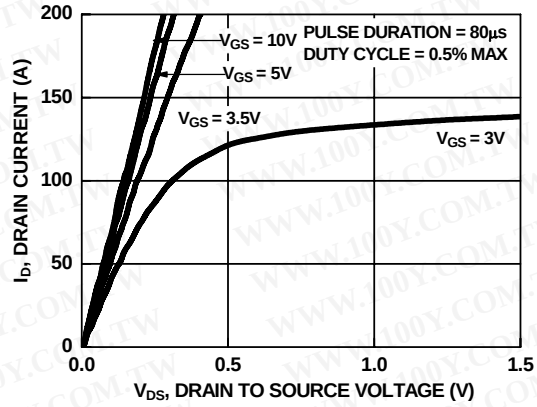


Figure 8. Saturation Characteristics

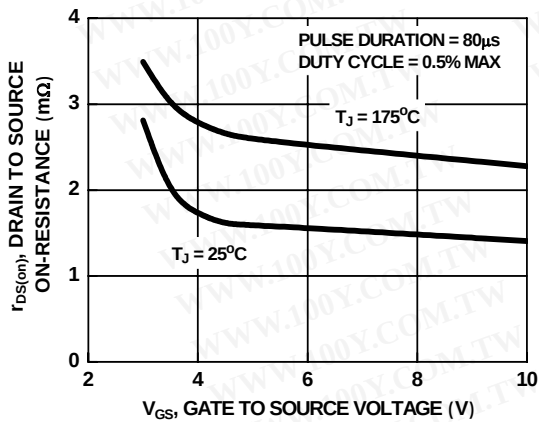


Figure 9. Drain to Source On-Resistance Variation vs Gate to Source Voltage

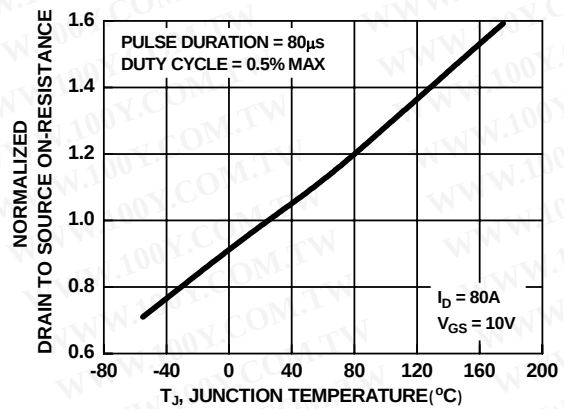


Figure 10. Normalized Drain to Source On-Resistance vs Junction Temperature

Typical Characteristics

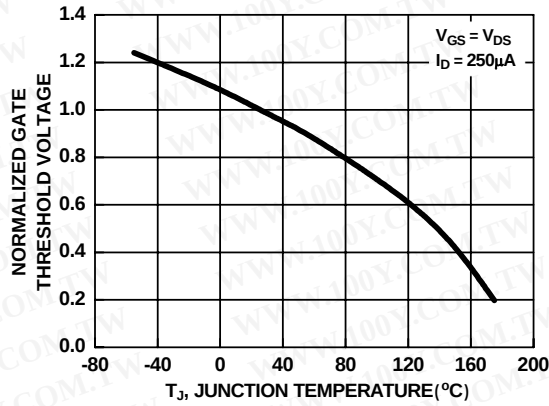


Figure 11. Normalized Gate Threshold Voltage vs Junction Temperature

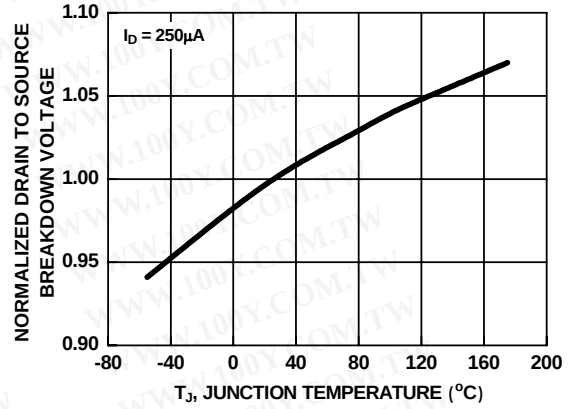


Figure 12. Normalized Drain to Source Breakdown Voltage vs Junction Temperature

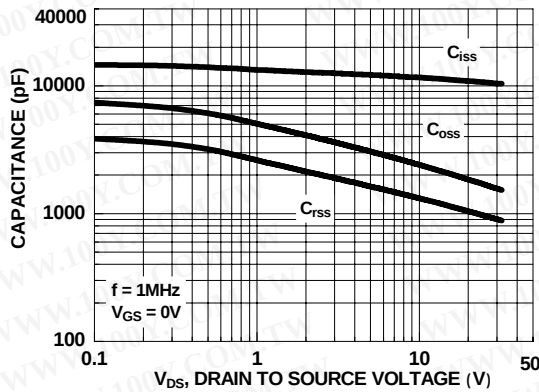


Figure 13. Capacitance vs Drain to Source Voltage

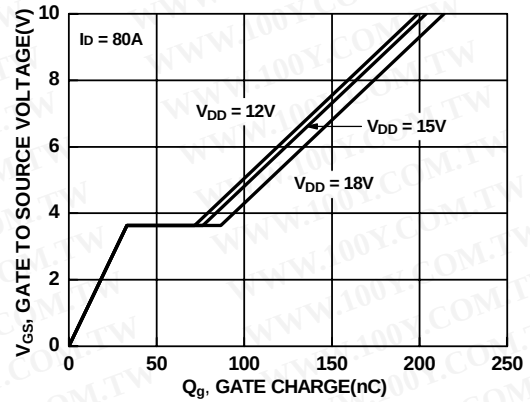


Figure 14. Gate Charge vs Gate to Source Voltage

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACE [™]	FACT Quiet Series [™]	OCX [™]	SILENT SWITCHER [®]	UniFET [™]
ActiveArray [™]	GlobalOptoisolator [™]	OCXPro [™]	SMART START [™]	UltraFET [®]
Bottomless [™]	GTO [™]	OPTOLOGIC [®]	SPM [™]	VCX [™]
Build it Now [™]	HiSeC [™]	OPTOPLANAR [™]	Stealth [™]	Wire [™]
CoolFET [™]	I ² C [™]	PACMAN [™]	SuperFET [™]	
CROSSVOL [™]	i-Lo [™]	POP [™]	SuperSOT [™] -3	
DOMET [™]	ImpliedDisconnect [™]	Power247 [™]	SuperSOT [™] -6	
EcoSPARK [™]	IntelliMAX [™]	PowerEdge [™]	SuperSOT [™] -8	
E ² CMOS [™]	ISOPLANAR [™]	PowerSaver [™]	SyncFET [™]	
EnSigna [™]	LittleFET [™]	PowerTrench [®]	TCM [™]	
FACT [™]	MICROCOUPLER [™]	QFET [®]	TinyBoost [™]	
FAST [®]	MicroFET [™]	QS [™]	TinyBuck [™]	
FAST [™]	MicroPak [™]	QT Optoelectronics [™]	TinyPWM [™]	
FPS [™]	MICROWIRE [™]	Quiet Series [™]	TinyPower [™]	
FRFET [™]	MSX [™]	RapidConfigure [™]	TinyLogic [®]	
	MSXPro [™]	RapidConnect [™]	TINYOPTO [™]	
		μSerDes [™]	TruTranslation [™]	
		ScalarPump [™]	UHC [™]	
Across the board. Around the world.™				
The Power Franchise [®]				
Programmable Active Droop [™]				

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

Rev. I20