

FCD7N60 / FCU7N60

600V N-Channel MOSFET

Features

- 650V @ $T_J = 150^{\circ}\text{C}$
- Typ. $R_{ds(on)} = 0.53\Omega$
- Ultra low gate charge (typ. $Q_g = 23\text{nC}$)
- Low effective output capacitance (typ. $C_{oss,eff} = 60\text{pF}$)
- 100% avalanche tested
- RoHS Compliant



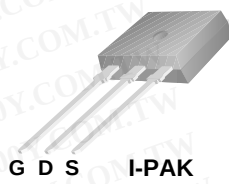
Description

SuperFET™ is, Fairchild's proprietary, new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance.

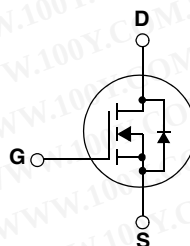
This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. Consequently, SuperFET is very suitable for various AC/DC power conversion in switching mode operation for system miniaturization and higher efficiency.



D-PAK
FCD Series



I-PAK
FCU Series



Absolute Maximum Ratings

Symbol	Parameter	FCD7N60/FCU7N60	Unit
V_{DSS}	Drain-Source Voltage	600	V
I_D	Drain Current - Continuous ($T_C = 25^{\circ}\text{C}$) - Continuous ($T_C = 100^{\circ}\text{C}$)	7 4.4	A A
I_{DM}	Drain Current - Pulsed (Note 1)	21	A
V_{GSS}	Gate-Source voltage	± 30	V
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	230	mJ
I_{AR}	Avalanche Current (Note 1)	7	A
E_{AR}	Repetitive Avalanche Energy (Note 1)	8.3	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	20	V/ns
P_D	Power Dissipation ($T_C = 25^{\circ}\text{C}$) - Derate above 25°C	83 0.67	W W/ $^{\circ}\text{C}$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150	$^{\circ}\text{C}$
T_L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds	300	$^{\circ}\text{C}$

Thermal Characteristics

Symbol	Parameter	FCD7N60/FCU7N60	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	1.5	$^{\circ}\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	83	$^{\circ}\text{C/W}$

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FCD7N60	FCD7N60TM	D-PAK	380mm	16mm	2500
FCD7N60	FCD7N60TF	D-PAK	380mm	16mm	2000
FCU7N60	FCU7N60	I-PAK	-	-	70

Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA, T _J = 25°C	600	--	--	V
		V _{GS} = 0V, I _D = 250μA, T _J = 150°C	--	650	--	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250μA, Referenced to 25°C	--	0.6	--	V/°C
BV _{DS}	Drain-Source Avalanche Breakdown Voltage	V _{GS} = 0V, I _D = 7A	--	700	--	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 600V, V _{GS} = 0V	--	--	1	μA
		V _{DS} = 480V, T _C = 125°C	--	--	10	μA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30V, V _{DS} = 0V	--	--	100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30V, V _{DS} = 0V	--	--	-100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	3.0	--	5.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10V, I _D = 3.5A	--	0.53	0.6	Ω
g _{FS}	Forward Transconductance	V _{DS} = 40V, I _D = 3.5A (Note 4)	--	6	--	S
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz	--	710	920	pF
C _{oss}	Output Capacitance		--	380	500	pF
C _{rss}	Reverse Transfer Capacitance		--	34	--	pF
C _{oss}	Output Capacitance	V _{DS} = 480V, V _{GS} = 0V, f = 1.0MHz	--	22	29	pF
C _{oss} eff.	Effective Output Capacitance	V _{DS} = 0V to 400V, V _{GS} = 0V	--	60	--	pF
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DD} = 300V, I _D = 7A R _G = 25Ω (Note 4, 5)	--	35	80	ns
t _r	Turn-On Rise Time		--	55	120	ns
t _{d(off)}	Turn-Off Delay Time		--	75	160	ns
t _f	Turn-Off Fall Time		--	32	75	ns
Q _g	Total Gate Charge	V _{DS} = 480V, I _D = 7A V _{GS} = 10V (Note 4, 5)	--	23	30	nC
Q _{gs}	Gate-Source Charge		--	4.2	5.5	nC
Q _{gd}	Gate-Drain Charge		--	11.5	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain-Source Diode Forward Current		--	--	7	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	21	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0V, I _S = 7A	--	--	1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _S = 7A di _P /dt = 100A/μs (Note 4)	--	360	--	ns
Q _{rr}	Reverse Recovery Charge		--	4.5	--	μC

NOTES:

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. $I_{AS} = 3.5A, V_{DD} = 50V, R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$
3. $I_{SD} \leq 7A, di/dt \leq 1200A/\mu s, V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$
4. Pulse Test: Pulse width $\leq 300\mu s$, Duty Cycle $\leq 2\%$
5. Essentially Independent of Operating Temperature

Typical Performance Characteristics

Figure 1. On-Region Characteristics

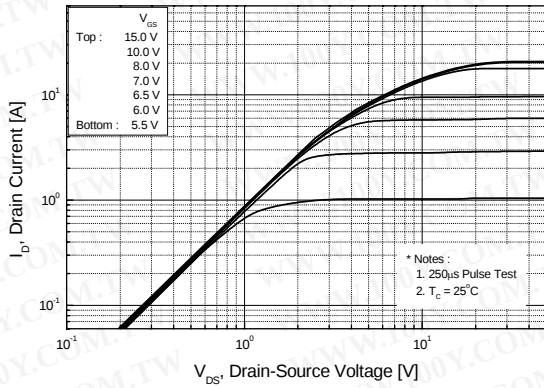


Figure 2. Transfer Characteristics

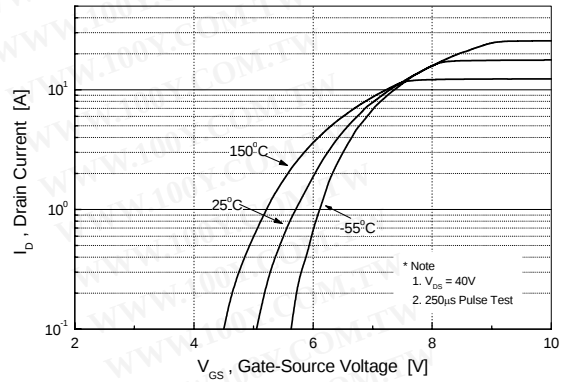


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

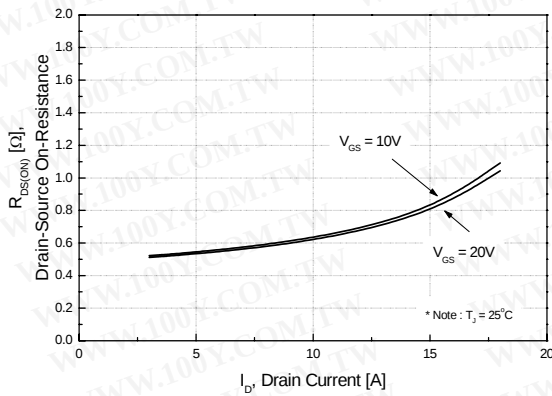


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

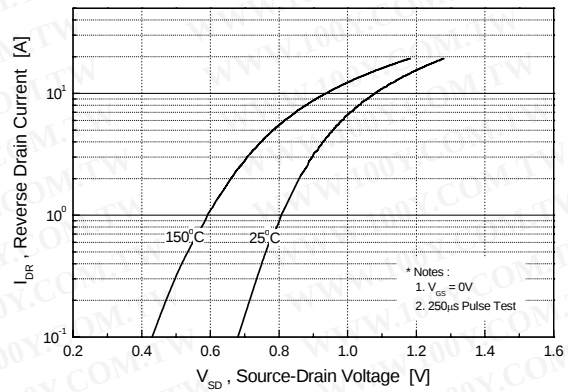


Figure 5. Capacitance Characteristics

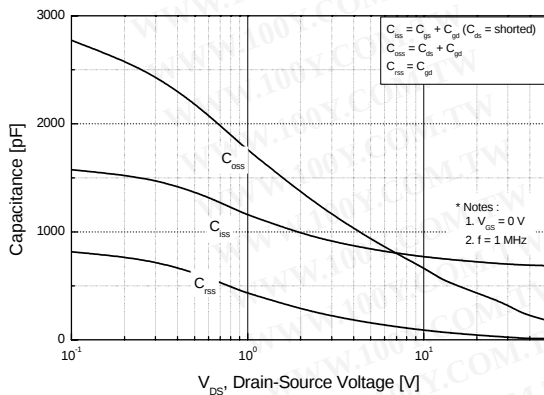
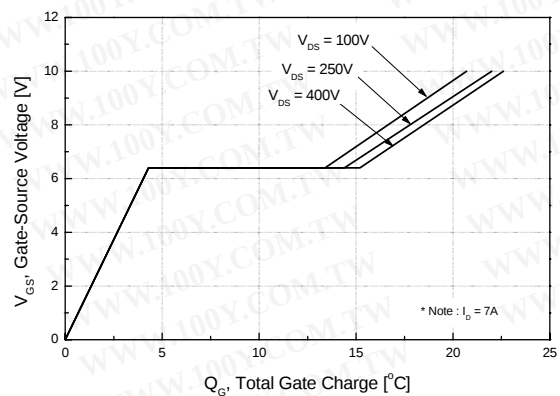


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

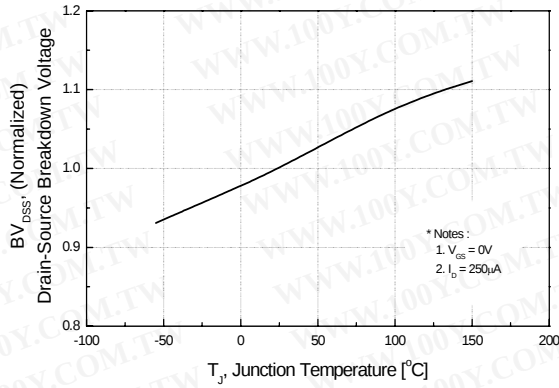


Figure 8. On-Resistance Variation vs. Temperature

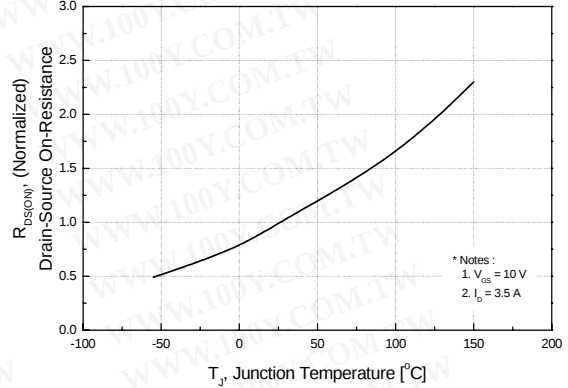


Figure 9. Maximum Safe Operating Area

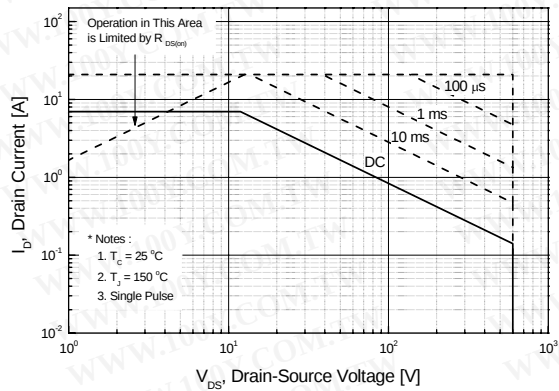


Figure 10. Maximum Drain Current vs. Case Temperature

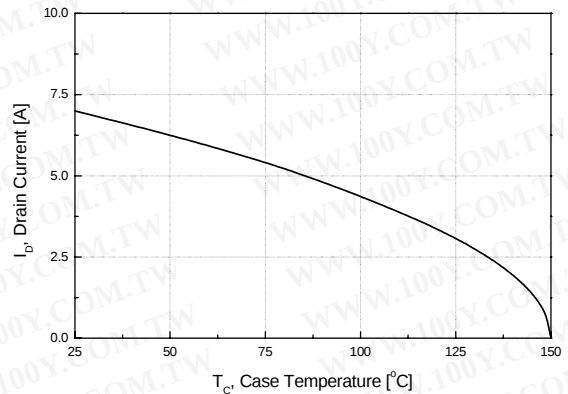
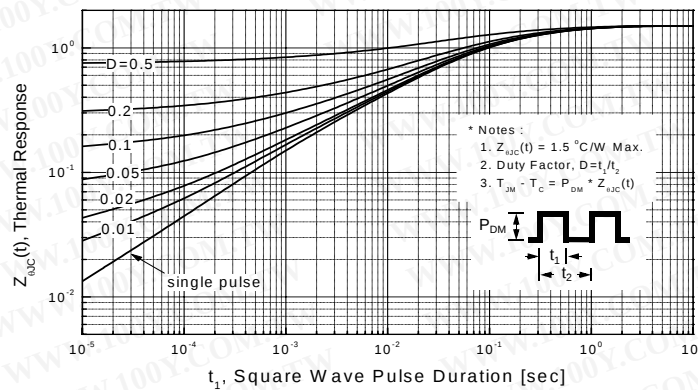
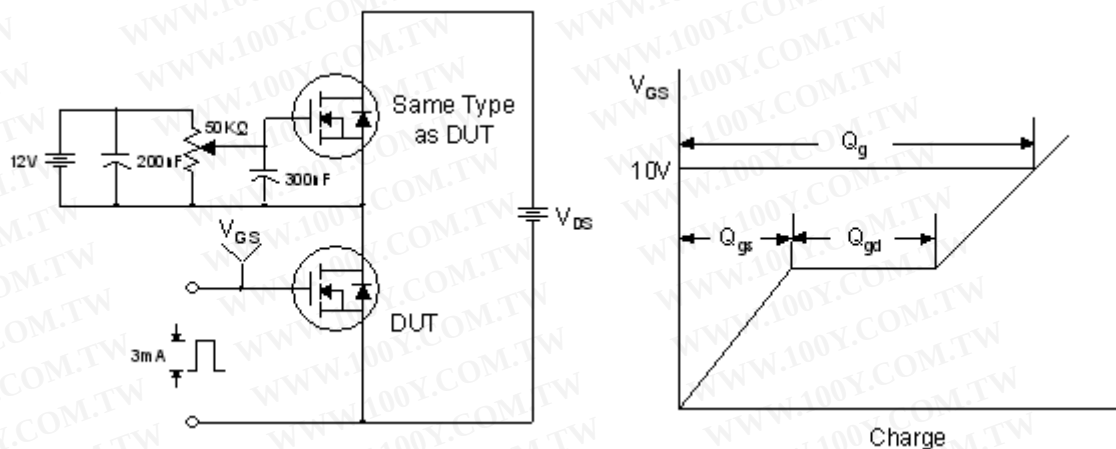


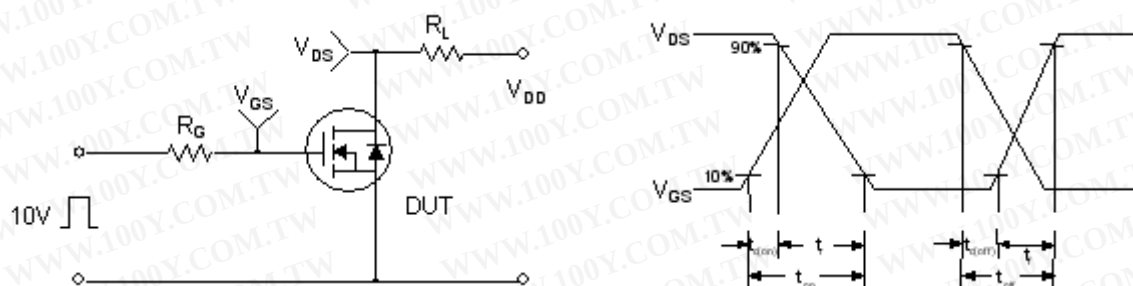
Figure 11. Transient Thermal Response Curve



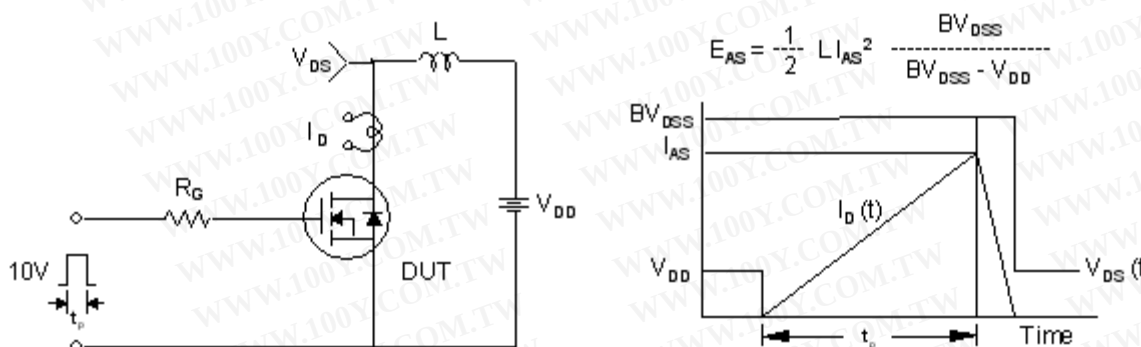
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

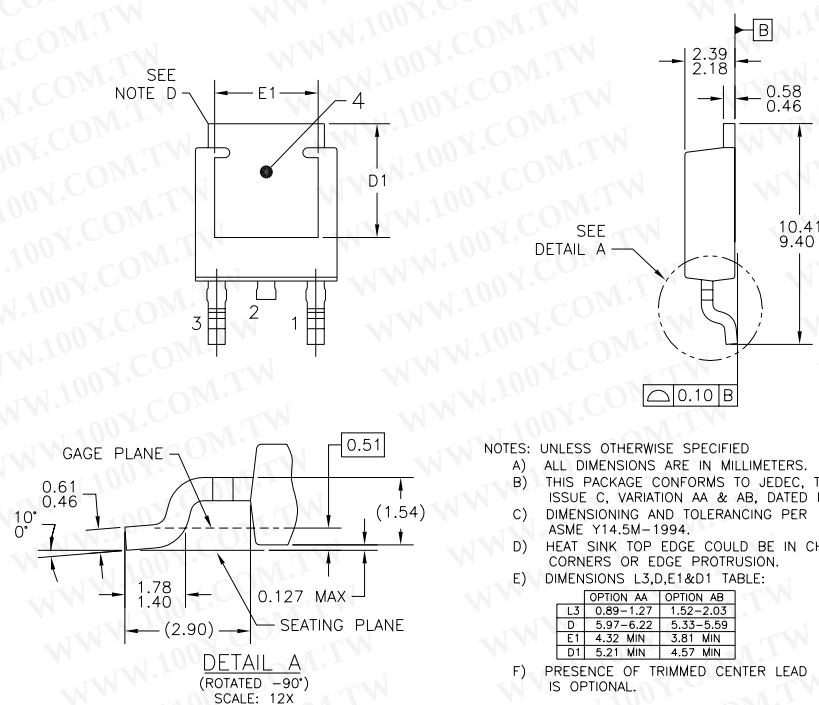
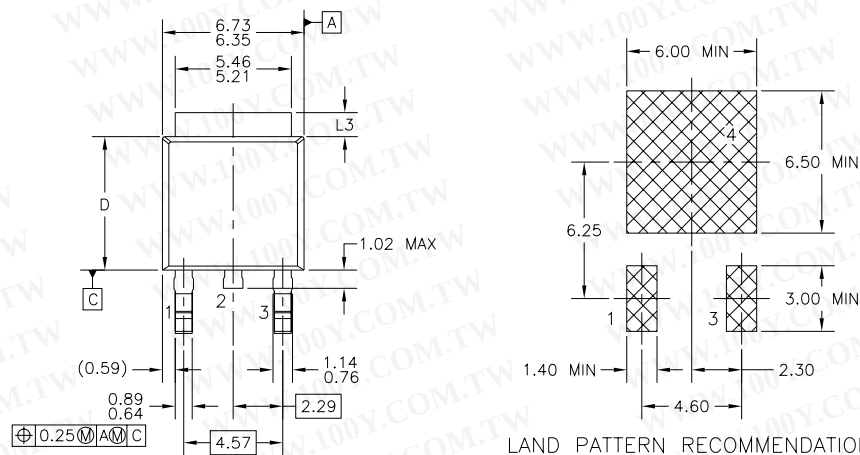


Unclamped Inductive Switching Test Circuit & Waveforms



Mechanical Dimensions

D-PAK



NOTES: UNLESS OTHERWISE SPECIFIED

A) ALL DIMENSIONS ARE IN MILLIMETERS.

B) THIS PACKAGE CONFORMS TO JEDEC, TO-252,
ISSUE C, VARIATION AA & AB, DATED NOV. 1999.

C) DIMENSIONING AND TOLERANCING PER
ASME Y14.5M-1994.

D) HEAT SINK TOP EDGE COULD BE IN CHAMFERED CORNERS OR EDGE PROTRUSION.

E) DIMENSIONS L3.D.E1&D1 TABLE:

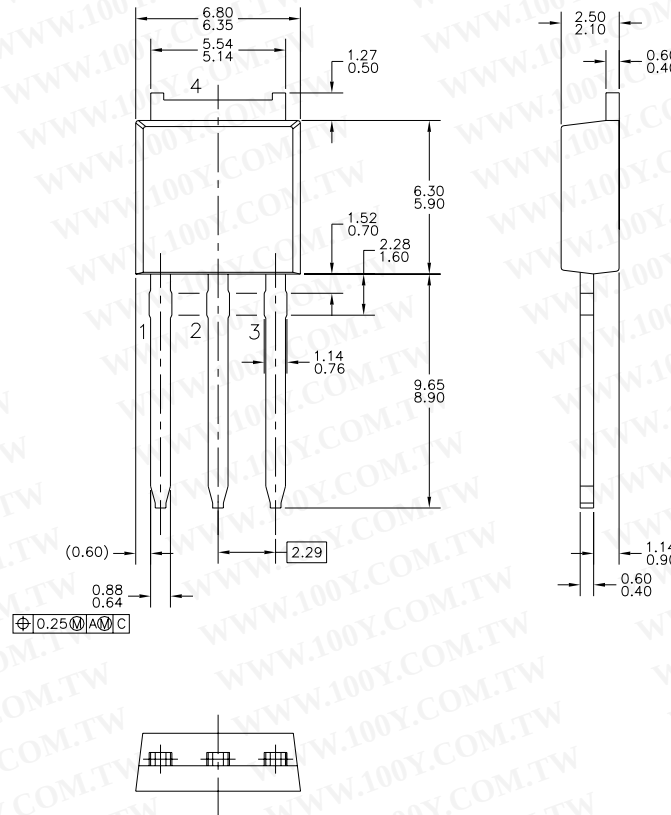
	OPTION AA	OPTION AB
L3	0.89-1.27	1.52-2.03
D	5.97-6.22	5.33-5.59
E1	4.32 MIN	3.81 MIN
D1	5.21 MIN	4.57 MIN

F) PRESENCE OF TRIMMED CENTER LEAD IS OPTIONAL.

Dimensions in Millimeters

Package Dimensions (Continued)

I-PAK



Dimensions in Millimeters



勝特力材料 886-3-5753170
 勝特力电子(上海) 86-21-34970699
 勝特力电子(深圳) 86-755-83298787
[Http://www.100y.com.tw](http://www.100y.com.tw)

TRADEMARKS

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

Build it Now™
 CorePLUS™
 CorePOWER™
 CROSSVOLT™
 CTL™
 Current Transfer Logic™
 EcoSPARK®
 EfficientMax™
 EZSWITCH™ *



Fairchild®
 Fairchild Semiconductor®
 FACT Quiet Series™
 FACT®
 FAST®
 FastvCore™
 FlashWriter® *
 FPS™
 F-PFS™

FRFET®
 Global Power Resource™
 Green FPS™
 Green FPS™ e-Series™
 GTO™

IntelliMAX™
 ISOPLANAR™
 MegaBuck™
 MICROCOUPLER™
 MicroFET™
 MicroPak™
 MillerDrive™
 MotionMax™
 Motion-SPM™
 OPTOLOGIC®
 OPTOPLANAR®

PDP SPM™
 Power-SPM™
 PowerTrench®
 PowerXS™

Programmable Active Droop™
 QFET®
 QS™
 Quiet Series™
 RapidConfigure™



Saving our world, 1mW /W /kW at a time™
 SmartMax™
 SMART START™
 SPM®
 STEALTH™
 SuperFET™
 SuperSOT™-3
 SuperSOT™-6
 SuperSOT™-8
 SupreMOS™
 SyncFET™



The Power Franchise®

the power
 franchise
 TinyBoost™
 TinyBuck™
 TinyLogic®
 TINYOPTO™
 TinyPower™
 TinyPWM™
 TinyWire™
 μSerDes™



UHC®
 Ultra FRFET™
 UniFET™
 VCX™
 VisualMax™
 XS™

* EZSWITCH™ and FlashWriter® are trademarks of System General Corporation, used under license by Fairchild Semiconductor.

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.fairchildsemi.com, under Sales Support.

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufactures of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed application, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handling and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address and warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

Rev. I37