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Published by Prentice Hall PTR
Series: Prentice Hall Modern Semiconductor Design Series

ISBN: 0130619701; Published: Jan 14, 2002; Copyright 2002;
Dimensions K; Pages: 640; Edition: 3rd.

Book Description

Modern VLSI Design, System-on-Chip Design, third edition, is a comprehensive, "bottom-up" guide to the entire VLSI design process, focusing on state-of-the-art SoC techniques that maximize performance, reduce power usage, and minimize time to market. New coverage includes: the latest copper and advanced interconnect models, practical IP-based design guidance, Verilog and VHDL overviews, plus detailed coverage of testing at all levels of abstraction.